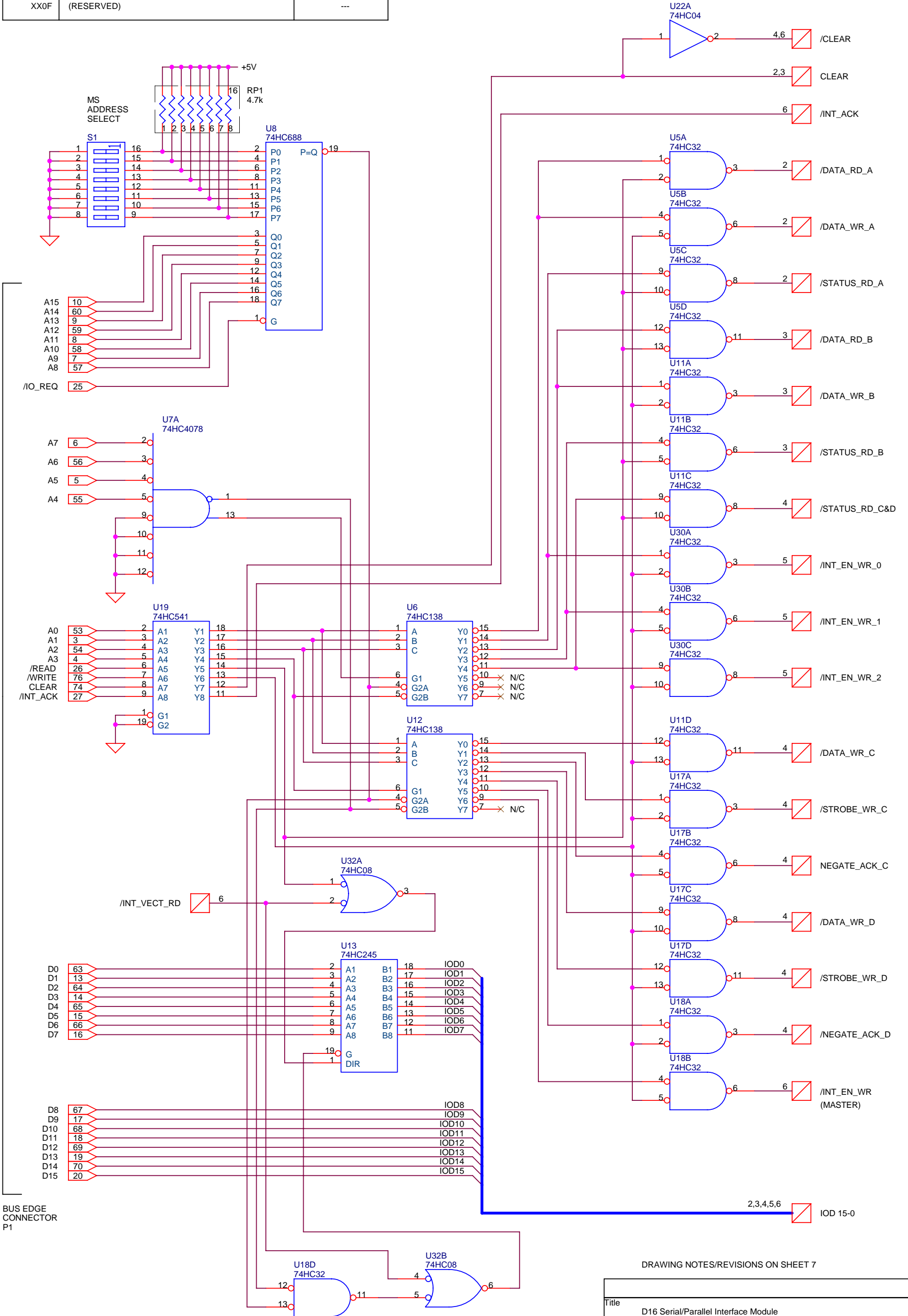
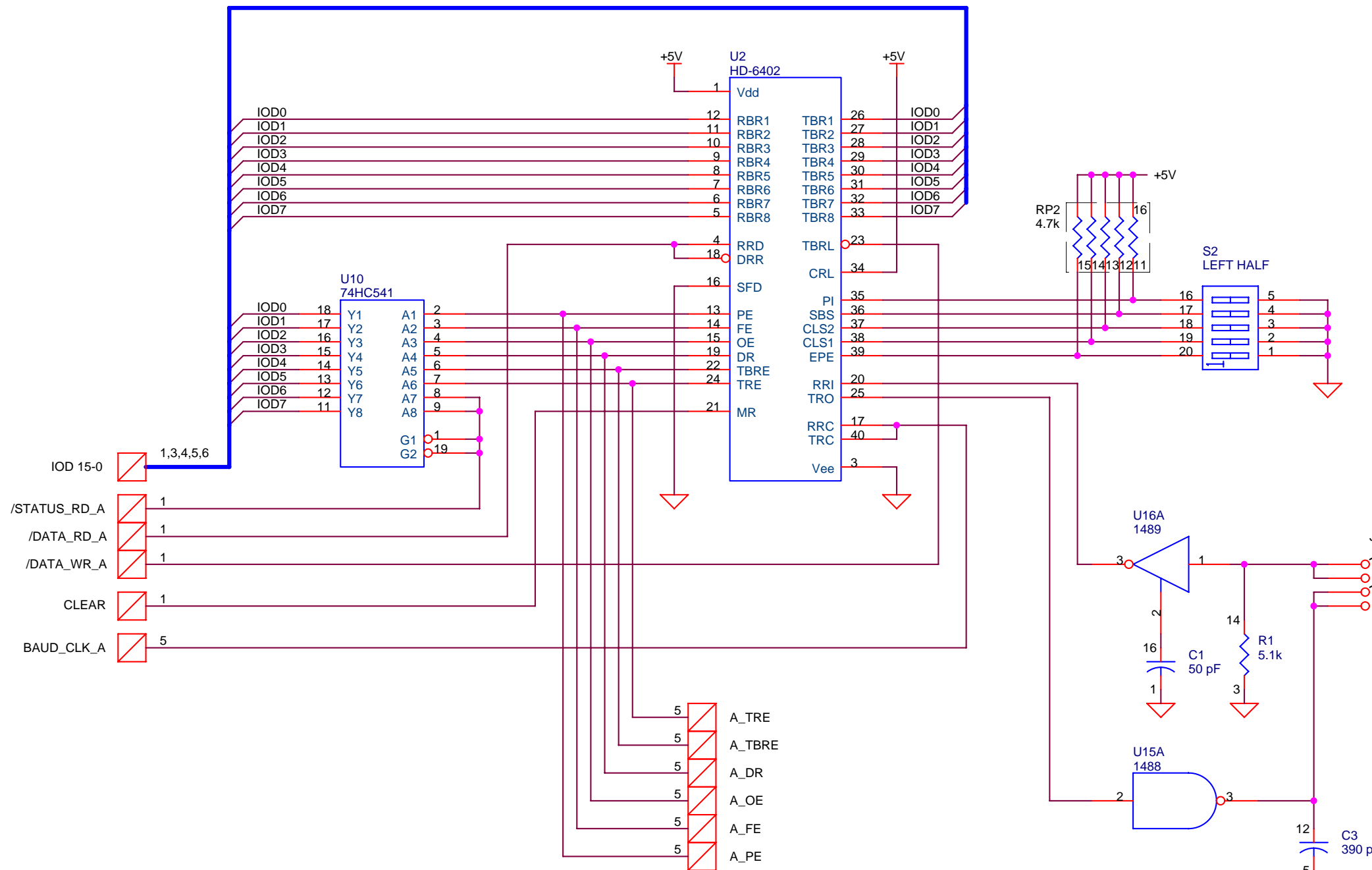


ADDRESS	FUNCTION	TYPE
XX00	SERIAL PORT A DATA	READ/WRITE
XX01	SERIAL PORT A STATUS/INTERRUPT ENABLE	READ/WRITE
XX02	SERIAL PORT B DATA	READ/WRITE
XX03	SERIAL PORT B STATUS/INTERRUPT ENABLE	READ/WRITE
XX04	PARALLEL PORTS C&D STATUS/INTERRUPT ENABLE	READ/WRITE
XX05	(RESERVED)	---
XX06	(RESERVED)	---
XX07	(RESERVED)	---
XX08	PARALLEL PORT C DATA	WRITE
XX09	PARALLEL PORT C STROBE	WRITE
XX0A	PARALLEL PORT C NEGATE ACK	WRITE
XX0B	PARALLEL PORT D DATA	WRITE
XX0C	PARALLEL PORT D STROBE	WRITE
XX0D	PARALLEL PORT D NEGATE ACK	WRITE
XX0E	MASTER INTERRUPT ENABLE	WRITE
XX0F	(RESERVED)	---



DRAWING NOTES/REVISIONS ON SHEET 7

Title		
D16 Serial/Parallel Interface Module		
Size	Document Number	Rev
	DE000004	1.1
Date:	Tuesday, April 26, 2005	Sheet 1 of 7

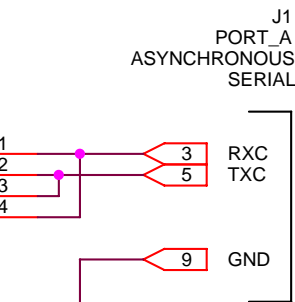


PI	FUNCTION
OPEN	PARITY INHIBIT
CLOSED	PARITY ENABLE

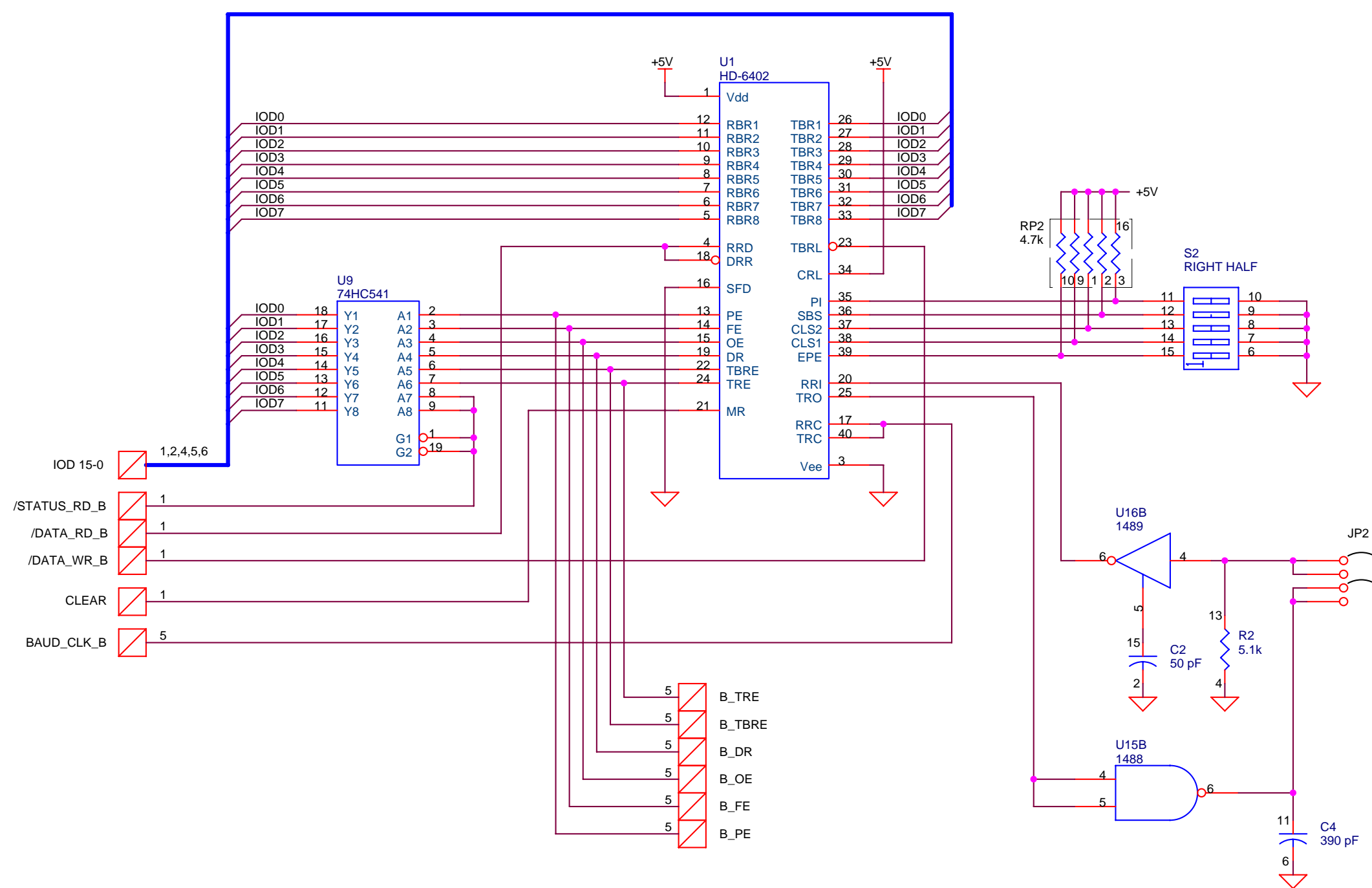
SBS	FUNCTION
OPEN	2 STOP BITS
CLOSED	1 STOP BIT

CLS2	CLS1	BITS/CHAR
OPEN	OPEN	8 BITS
OPEN	CLOSED	7 BITS
CLOSED	OPEN	6 BITS
CLOSED	CLOSED	5 BITS

EPE	FUNCTION
OPEN	EVEN PARITY
CLOSED	ODD PARITY



JP1 JUMPERS	MODE
1,3	DCE
2,4	DTE
1,2 or 3,4	LOOPBACK

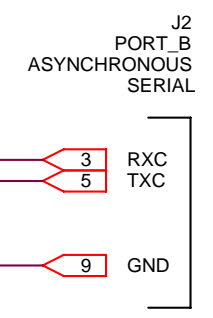


PI	FUNCTION
OPEN	PARITY INHIBIT
CLOSED	PARITY ENABLE

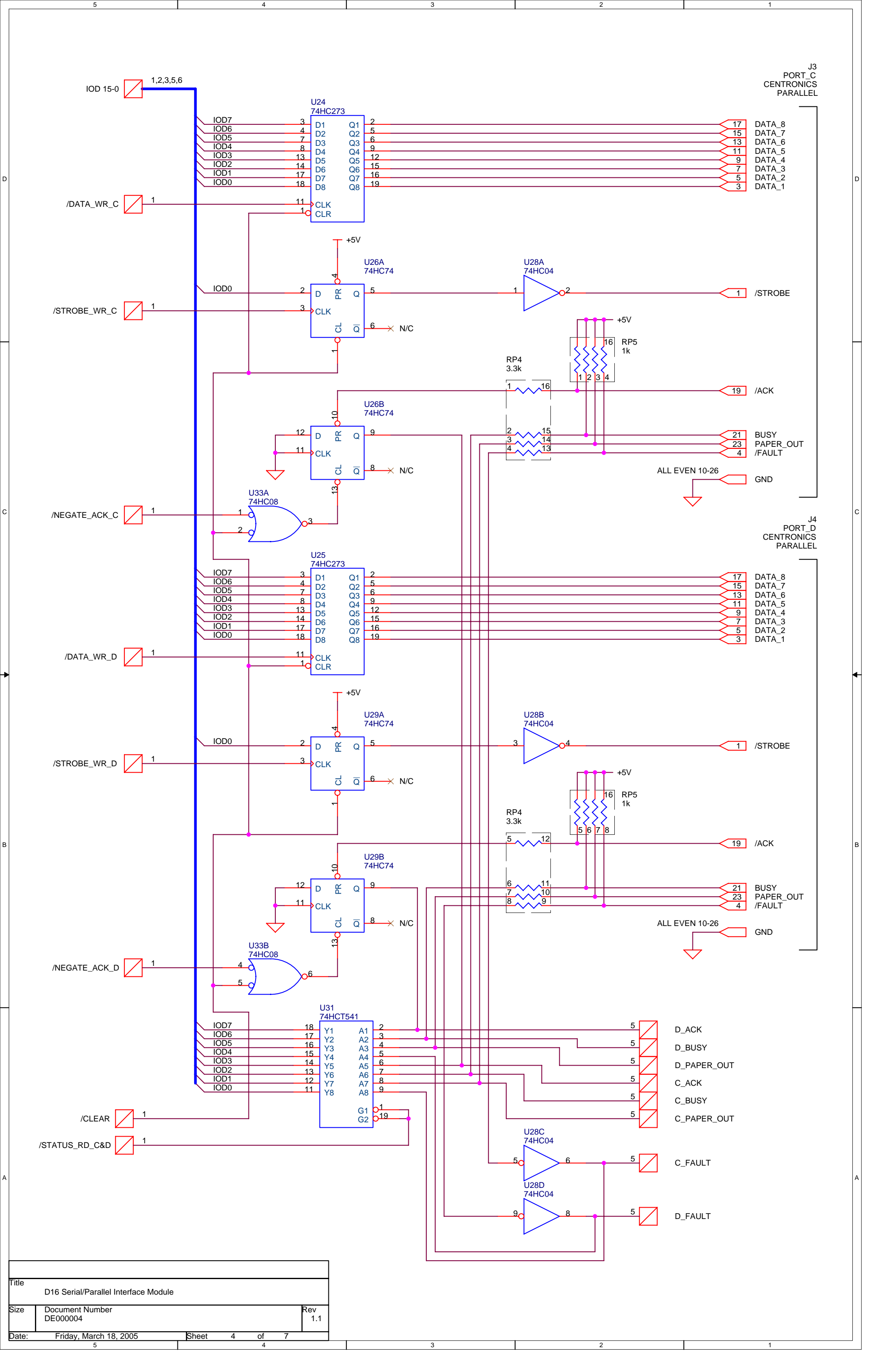
SBS	FUNCTION
OPEN	2 STOP BITS
CLOSED	1 STOP BIT

CLS2	CLS1	BITS/CHAR
OPEN	OPEN	8 BITS
OPEN	CLOSED	7 BITS
CLOSED	OPEN	6 BITS
CLOSED	CLOSED	5 BITS

EPE	FUNCTION
OPEN	EVEN PARITY
CLOSED	ODD PARITY

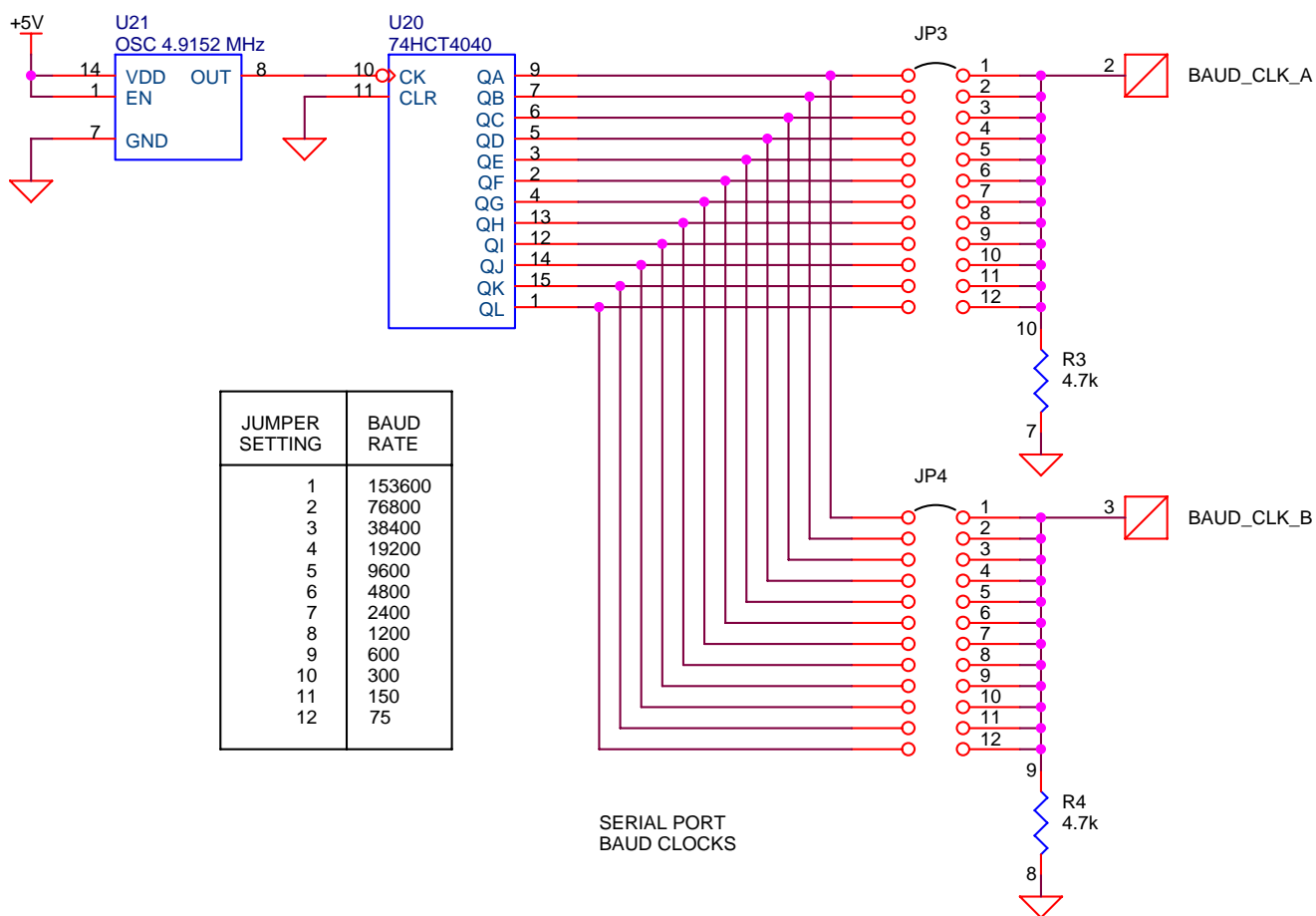
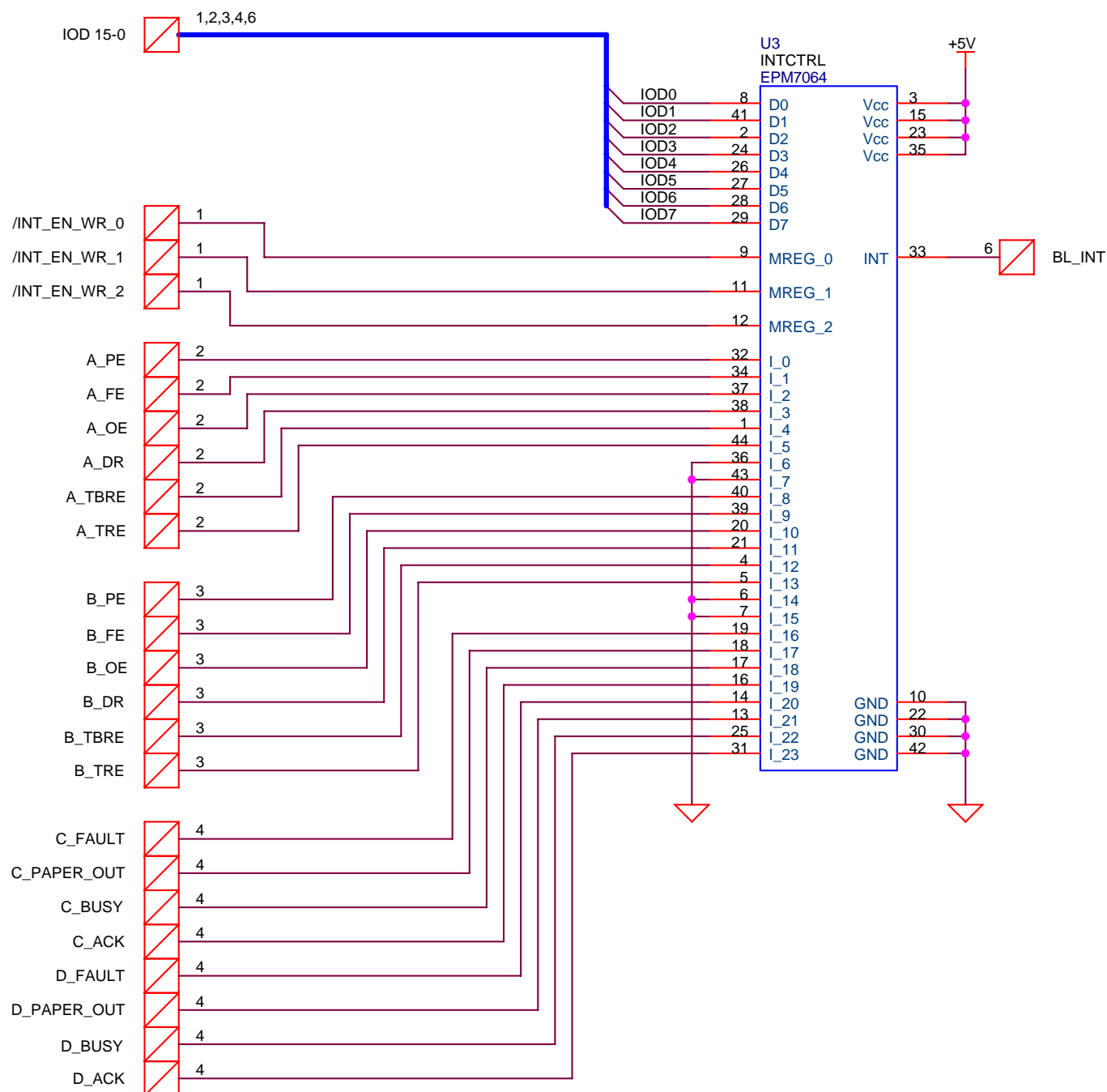


JP2 JUMPERS	MODE
1,3	DCE
2,4	DTE
1,2 or 3,4	LOOPBACK



Title		
D16 Serial/Parallel Interface Module		
Size	Document Number	Rev
	DE000004	1.1
Date:	Friday, March 18, 2005	Sheet 4 of 7

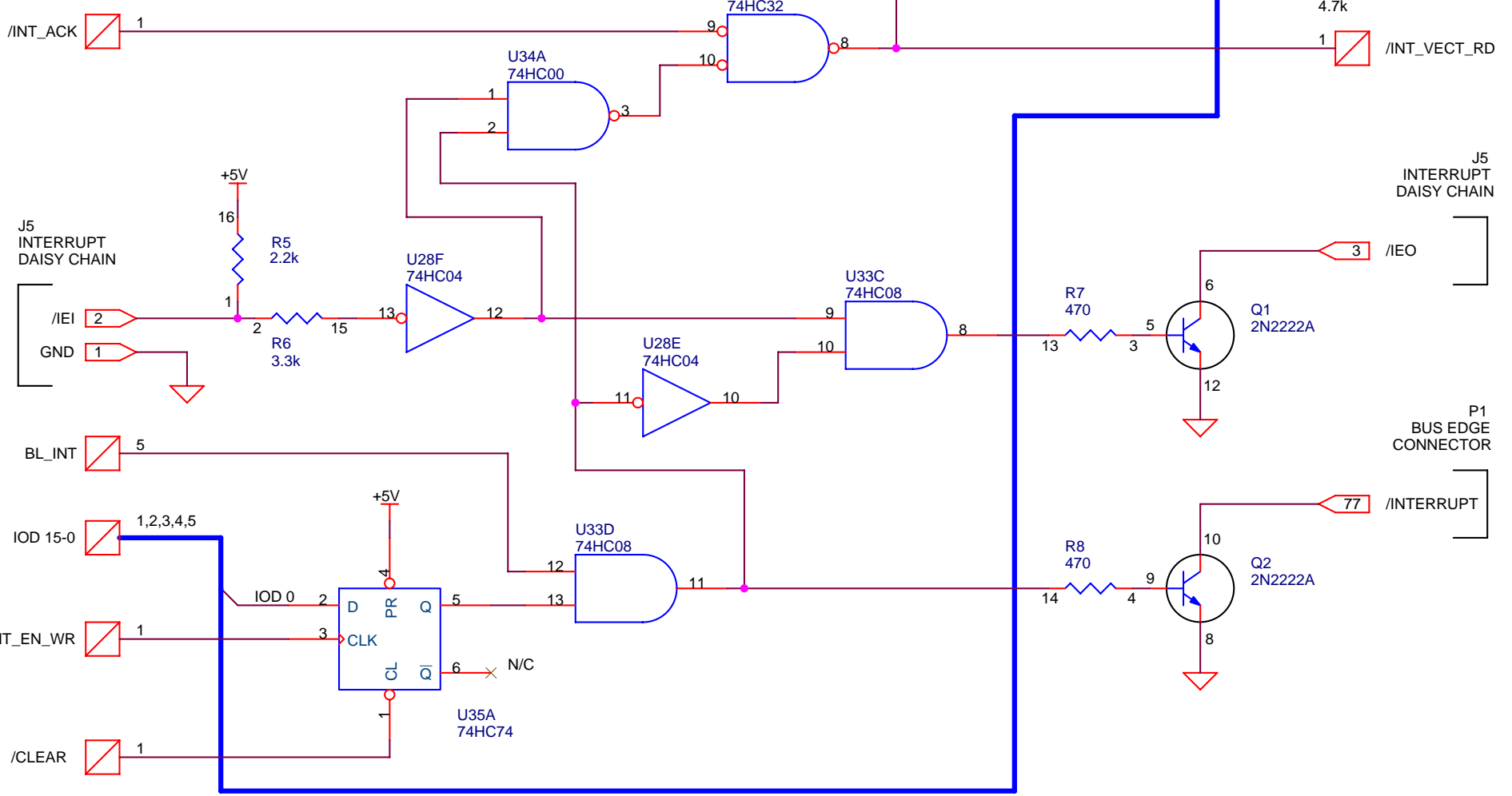
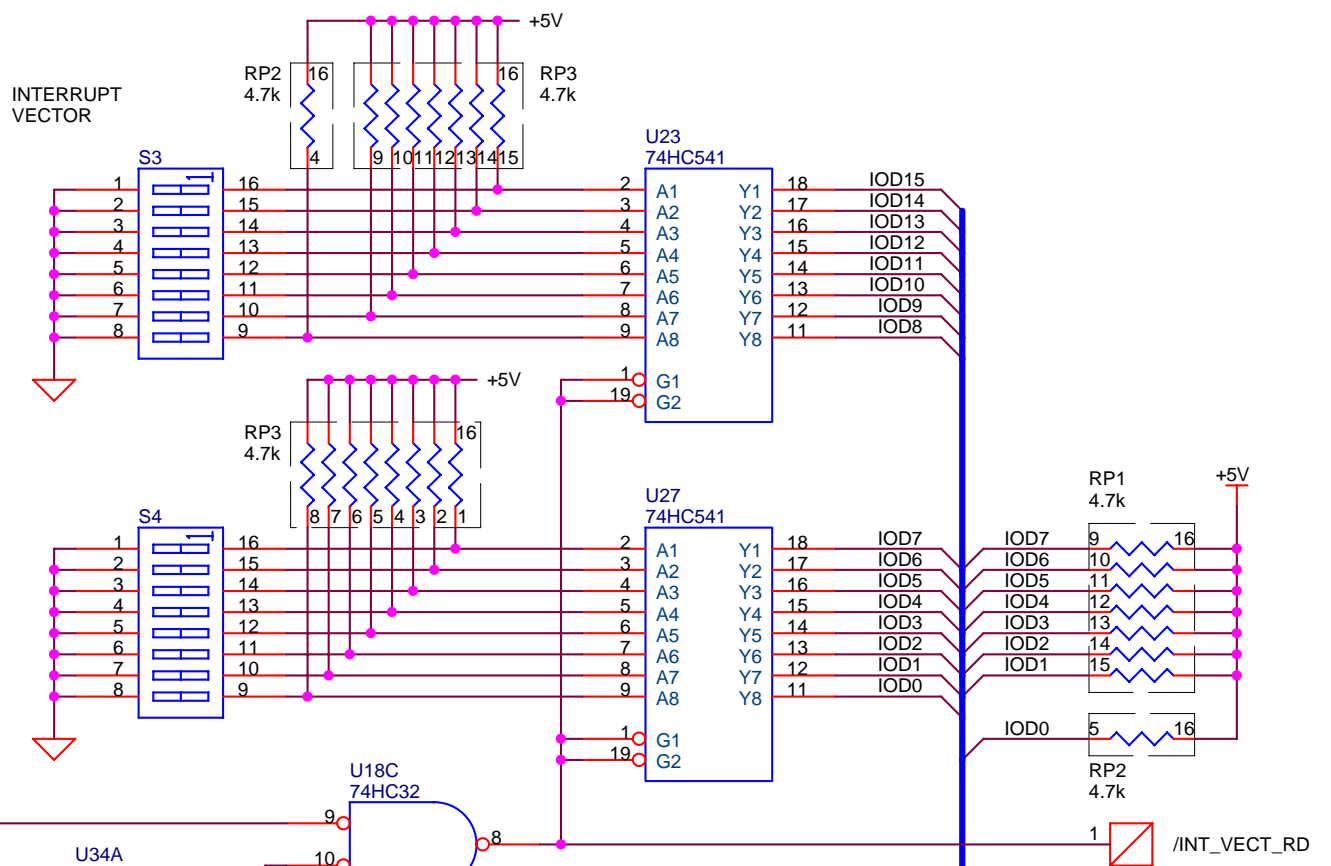
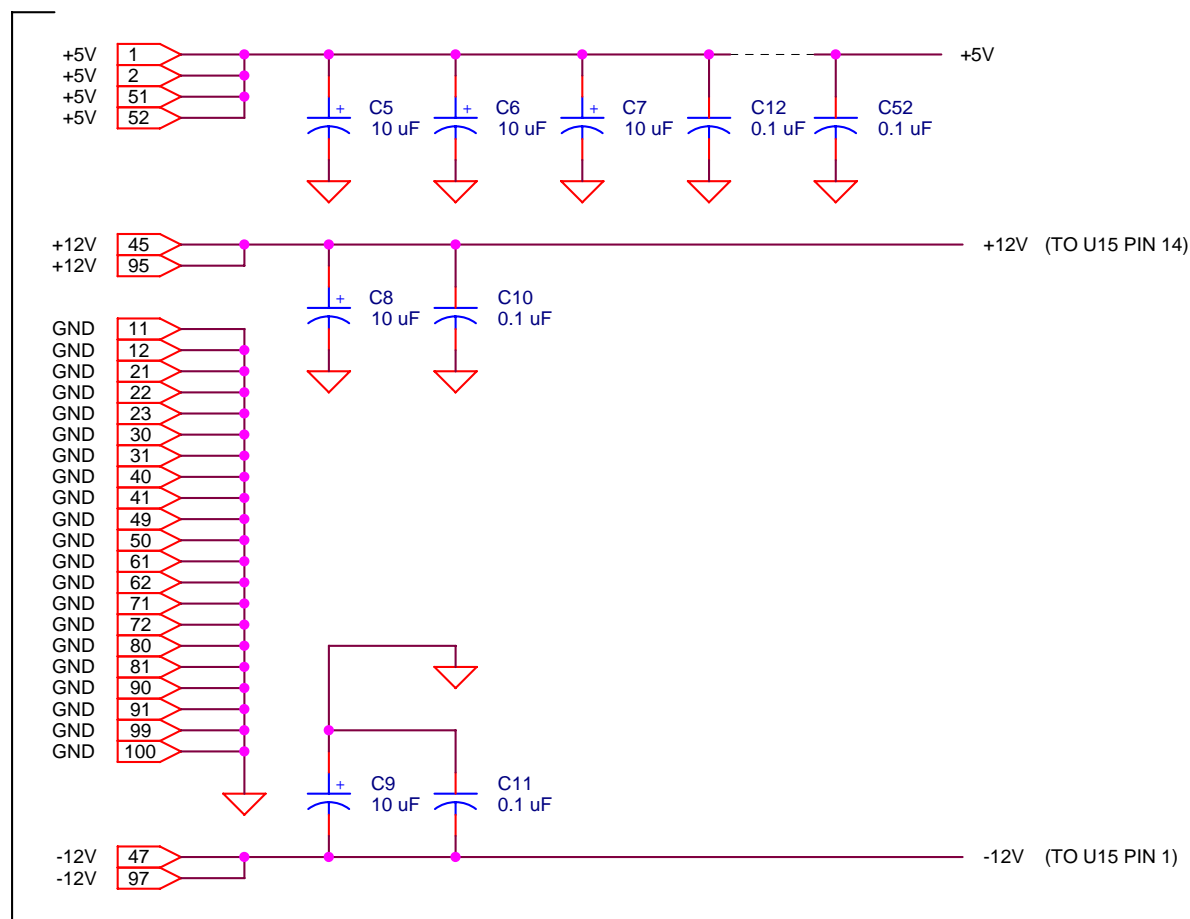
INTERRUPT CONTROL



Title		
D16 Serial/Parallel Interface Module		
Size	Document Number	Rev
	DE000004	1.1
Date:	Tuesday, April 26, 2005	Sheet 5 of 7

P1
BUS EDGE
CONNECTOR

POWER DISTRIBUTION



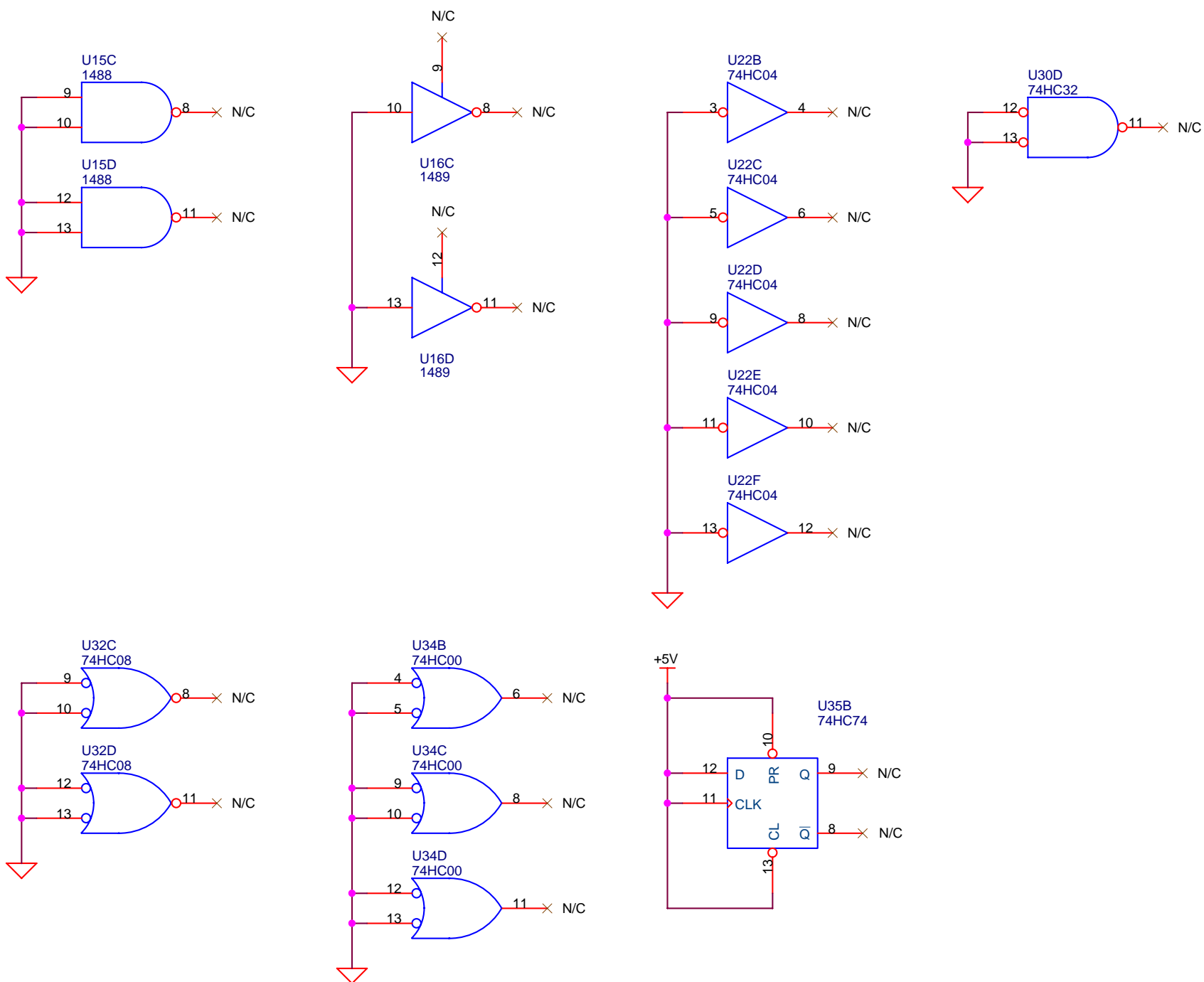
INTERRUPT PRIORITY
AND VECTOR LOGIC

Title		
D16 Serial/Parallel Interface Module		
Size	Document Number	Rev
	DE000004	1.1
Date:	Thursday, June 23, 2005	Sheet 6 of 7

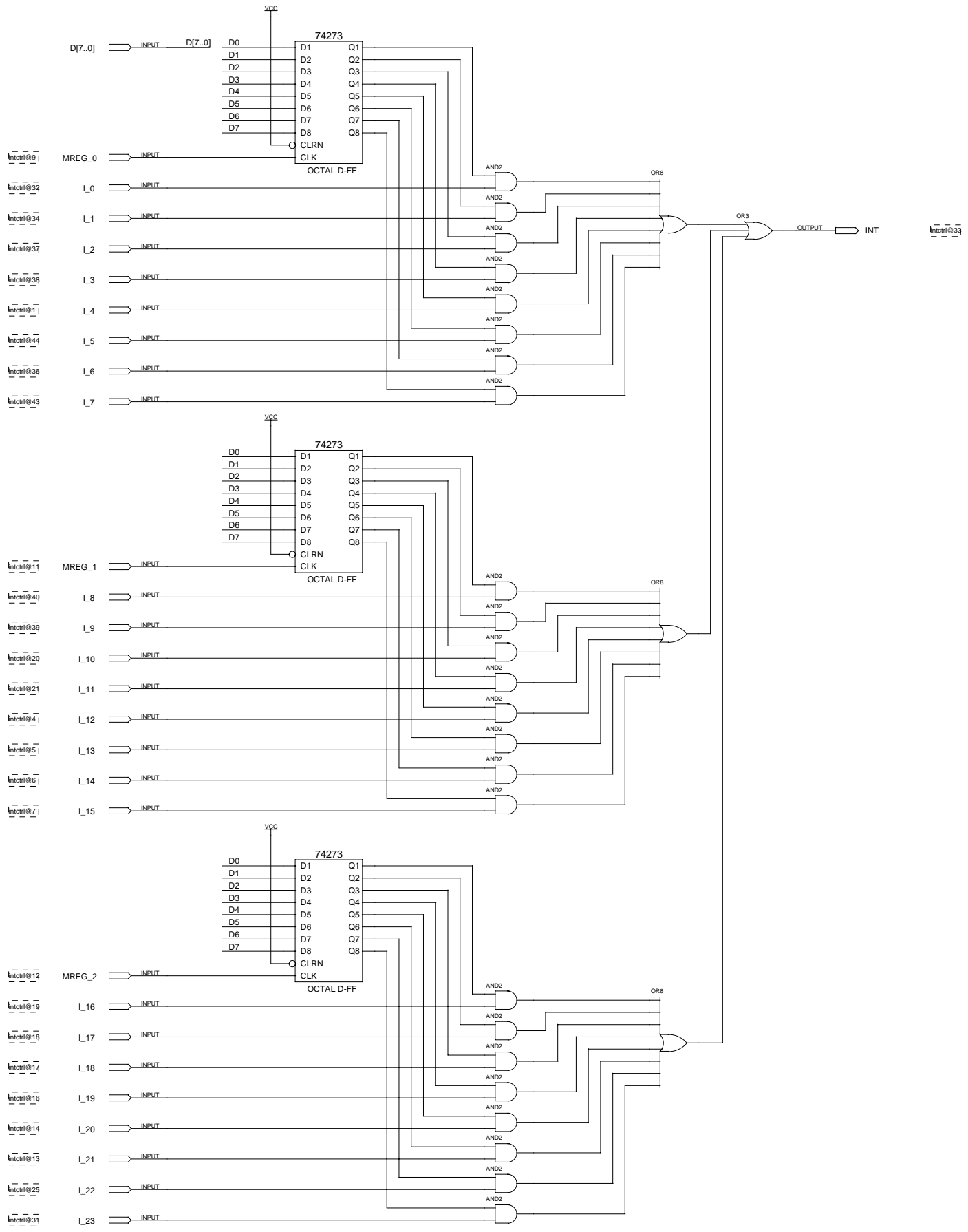
REV	DATE	CHANGES
1.0	05/06/04	BASIC COMPLETED DESIGN
1.1	06/23/05	DRAWING CLEANUP AFTER DEBUG

- NOTES:
- 1: ALL RESISTORS IN OHMS, 1/4W 5%
 - 2: ALL CAPACITORS IN MICROFARADS
ALL POLAR CAPACITORS TANTALUM, 25V 10%
ALL NON-POLAR CAPACITORS CERAMIC, 50V 10%
 - 3: DISCRETE COMPONENTS C1-C4, R1-R4 MOUNTED ON COMPONENT CARRIER CC1
 - 4: DISCRETE COMPONENTS R5-R8, Q1, Q2 MOUNTED ON COMPONENT CARRIER CC2

SPARE GATES



Title		
D16 Serial/Parallel Interface Module		
Size	Document Number	Rev
	DE000004	1.1
Date:	Thursday, June 23, 2005	Sheet 7 of 7



-DATA PINS-	-POWER PINS-
D0 8	Vcc: 3, 15, 23, 35
D1 41	GND: 10, 22, 30, 42
D2 2	
D3 24	
D4 26	
D5 27	
D6 28	
D7 29	

INTERRUPT CONTROLLER FOR D16 SPI MODULE			
TITLE INTCTRL			
COMPANY Epilog Corp.			
DESIGNER John Doran			
SIZE EV	NUMBER 1.00	REV 1.0	
DATE 9:13a 4-26-2005	SHEET 1	OF 1	