

- NOTES: 1: "PWR" AND "REG+" PLANES CONNECTED TOGETHER
 2: "GND" PLANES ON EACH SIDE OF BOARD CONNECTED TOGETHER
 3: FOIL REMOVED UNDER U3, JP3, AND JP4 TO PREVENT SHORT CIRCUITS
 4: CAPACITOR C12 MOUNTED UNDER IC U1 SOCKET
 CAPACITOR C13 MOUNTED UNDER IC U2 SOCKET
 5: VECTOR T44 WIRE-WRAP PINS INSTALLED AS SHOWN

D16/M SERIAL/PARALLEL I/O BOARD LAYOUT

