

D16 BUS  
(BOTTOM VIEW)

COMPONENT SIDES OF  
MODULES IN BUS FACE  
TOWARD PIN 1.

AUXILIARY BUS IS FOR  
USE BY PERIPHERALS.  
IT IS TERMINATED, BUT  
HAS NO CONNECTION  
TO THE D16 CPU.

51	+5V	+5V	1
52	+5V	+5V	2
53	A0	A1	3
54	A2	A3	4
55	A4	A5	5
56	A6	A7	6
57	A8	A9	7
58	A10	A11	8
59	A12	A13	9
60	A14	A15	10
61	GND	GND	11
62	GND	GND	12
63	D0	D1	13
64	D2	D3	14
65	D4	D5	15
66	D6	D7	16
67	D8	D9	17
68	D10	D11	18
69	D12	D13	19
70	D14	D15	20
71	GND	GND	21
72	GND	GND	22
73	/CLOCK	GND	23
74	CLEAR	/HALT	24
75	/MEM_REQ	/IO_REQ	25
76	/WRITE	/READ	26
77	/INTERRUPT	/INT_ACK	27
78	/STOP	/GO	28
79	/ROM_RAM	(RESERVED)	29
80	GND	GND	30
81	GND	GND	31
82	AUX0	AUX1	32
83	AUX2	AUX3	33
84	AUX4	AUX5	34
85	AUX6	AUX7	35
86	AUX8	AUX9	36
87	AUX10	AUX11	37
88	AUX12	AUX13	38
89	AUX14	AUX15	39
90	GND	GND	40
91	GND	GND	41
92	(N/C)	(N/C)	42
93	+3.3V	+3.3V	43
94	(N/C)	(N/C)	44
95	+12V	+12V	45
96	(N/C)	(N/C)	46
97	-12V	-12V	47
98	(N/C)	(N/C)	48
99	GND	GND	49
100	GND	GND	50

ADDRESS BUS

DATA BUS

CONTROL/STATUS BUS

AUXILIARY BUS