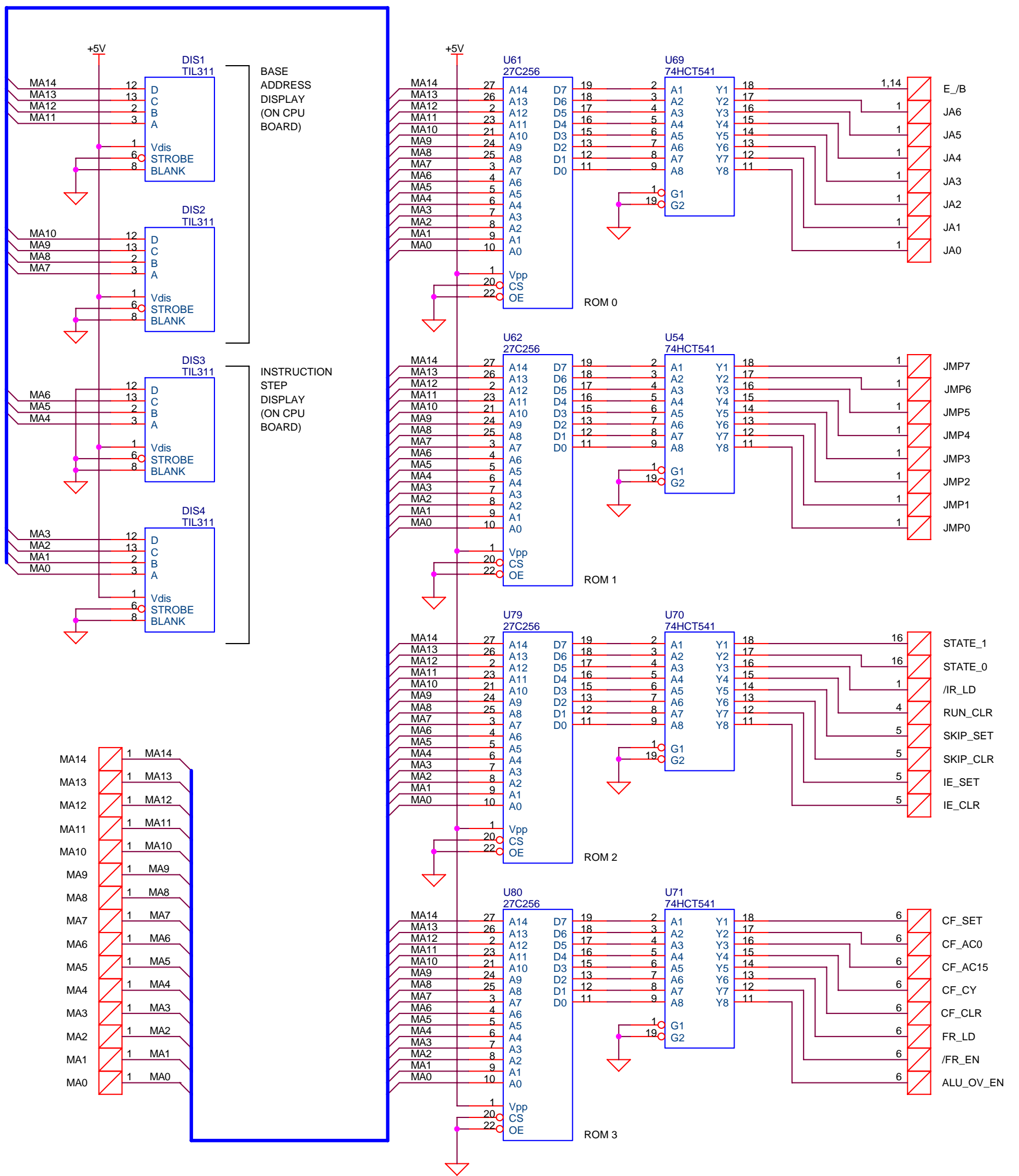
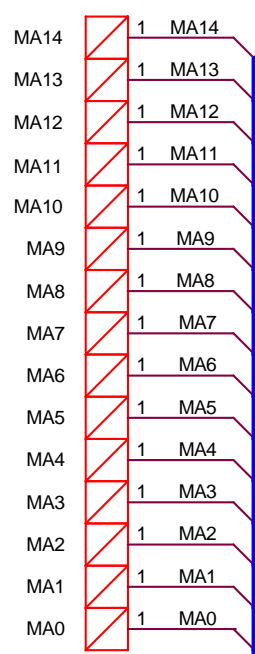
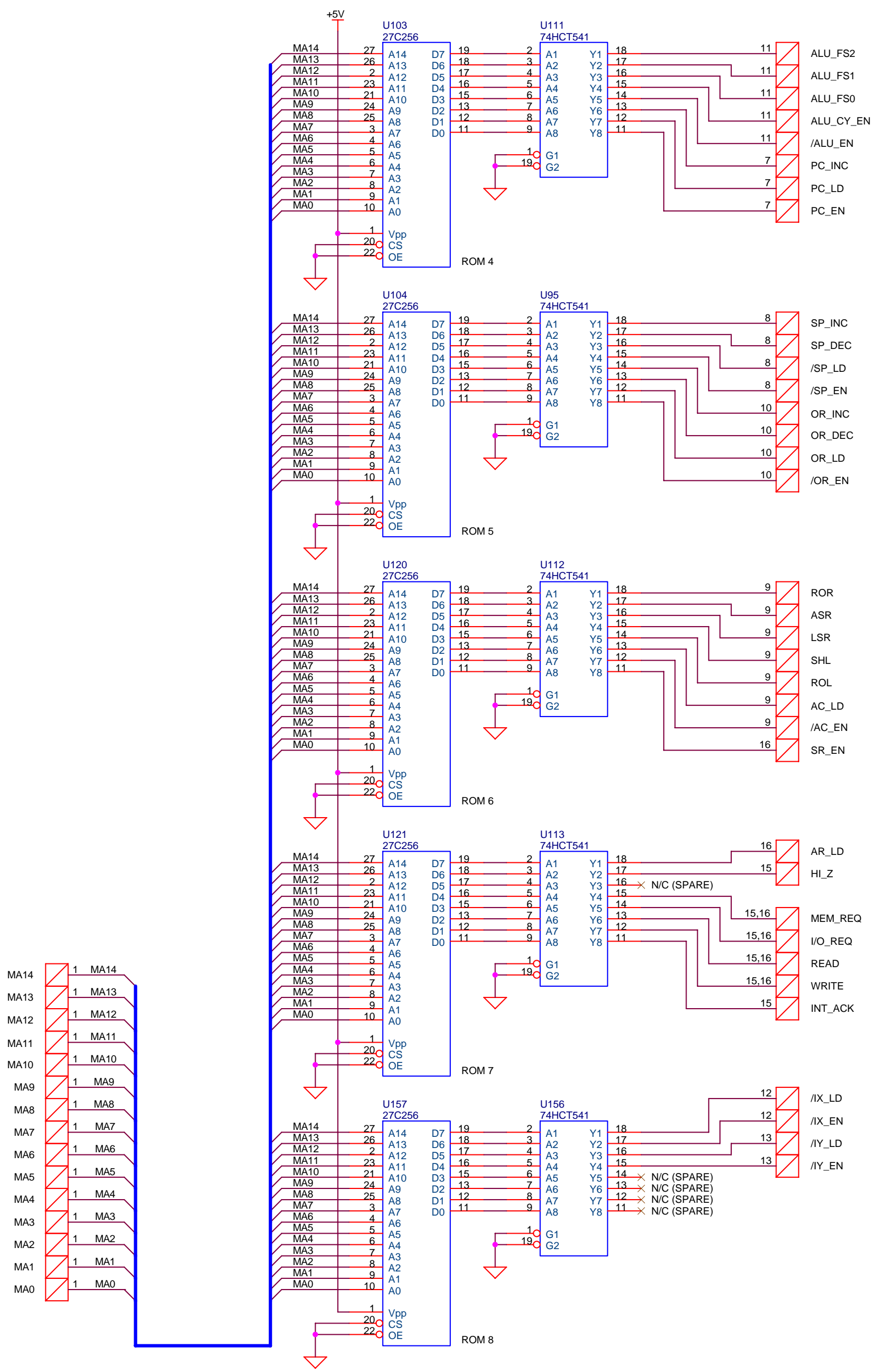
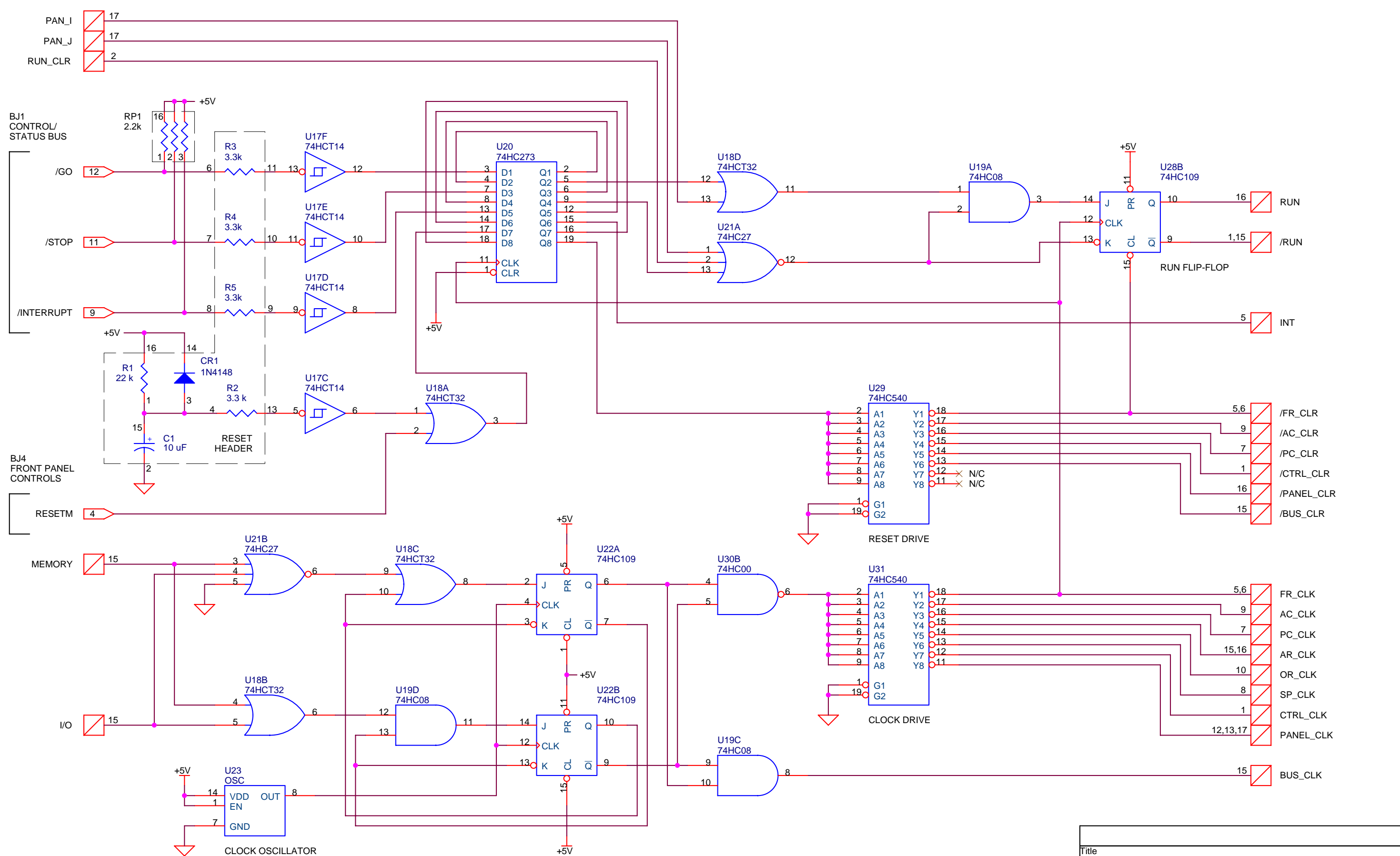


Title		
D16 PROCESSOR -- Microprogram Control Unit		
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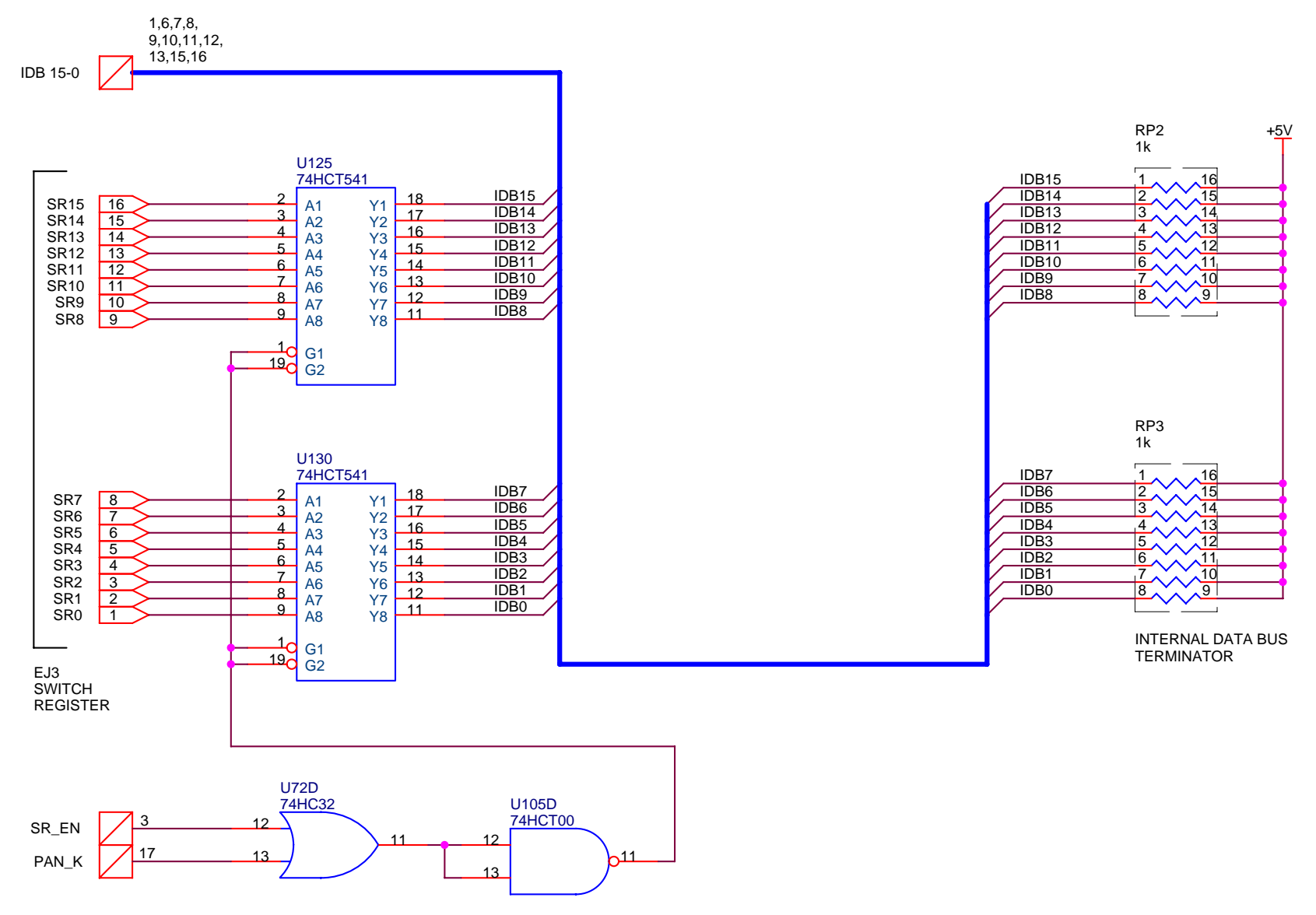
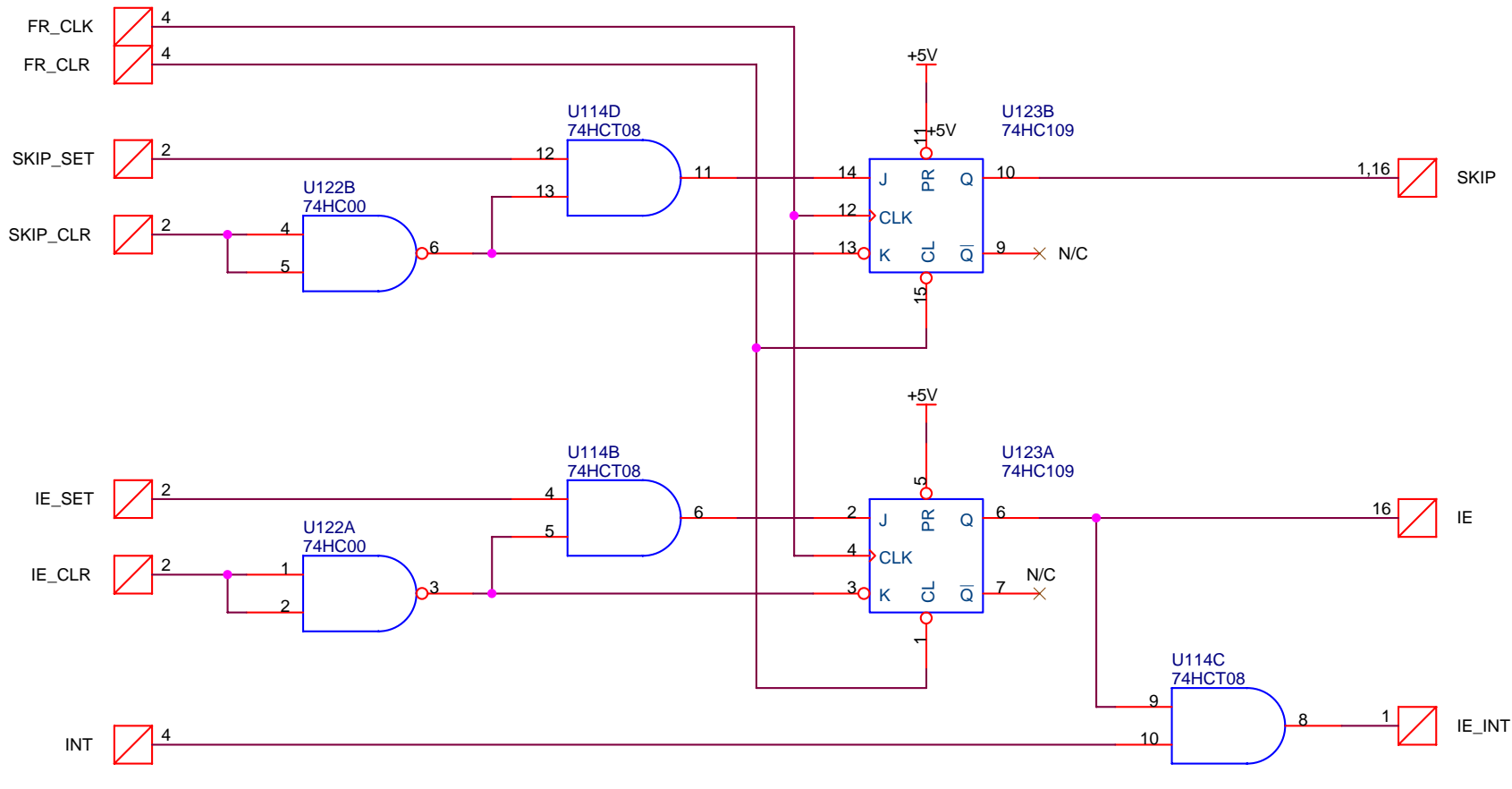
DRAWING NOTES AND REVISIONS ON SHEET 18



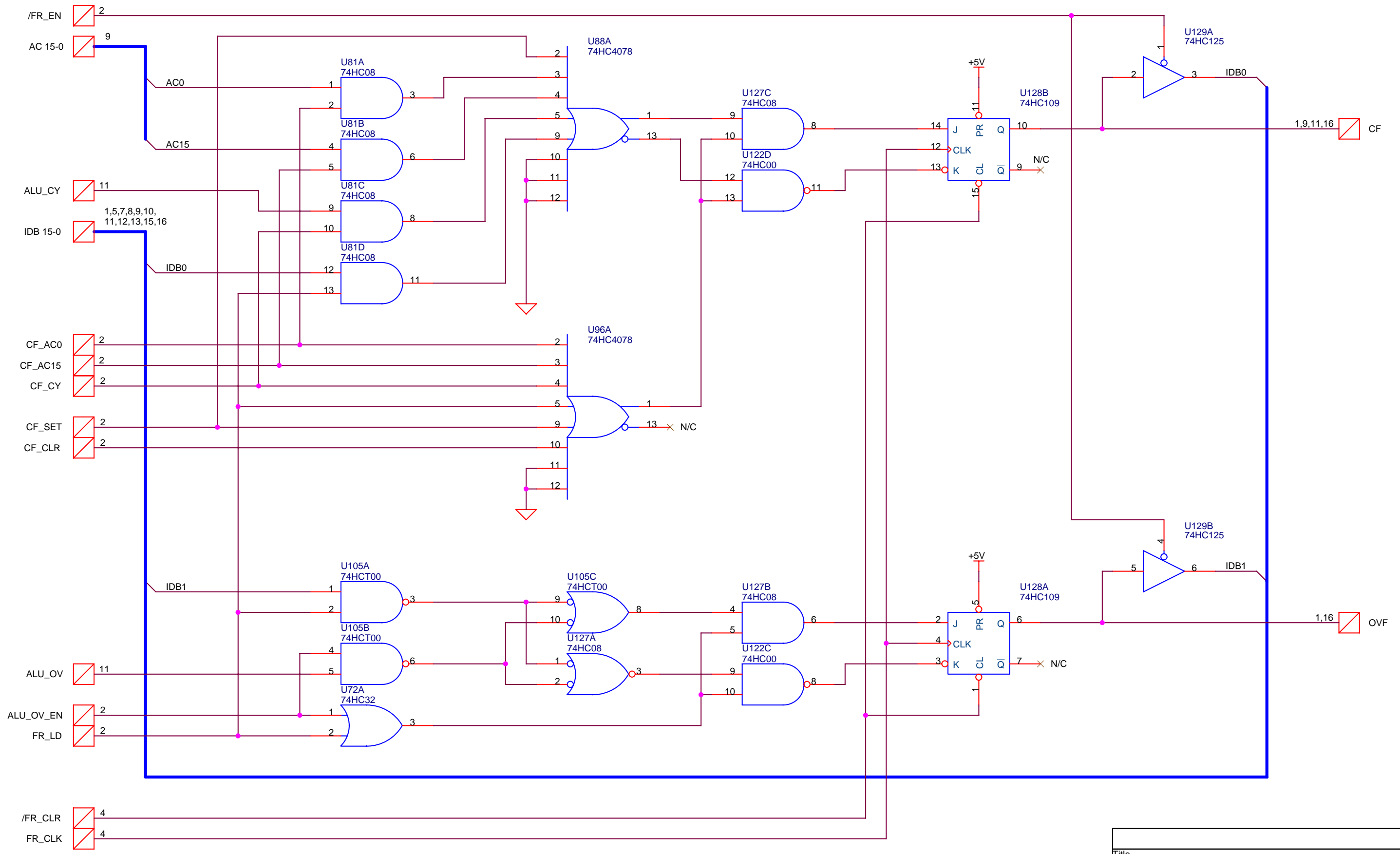




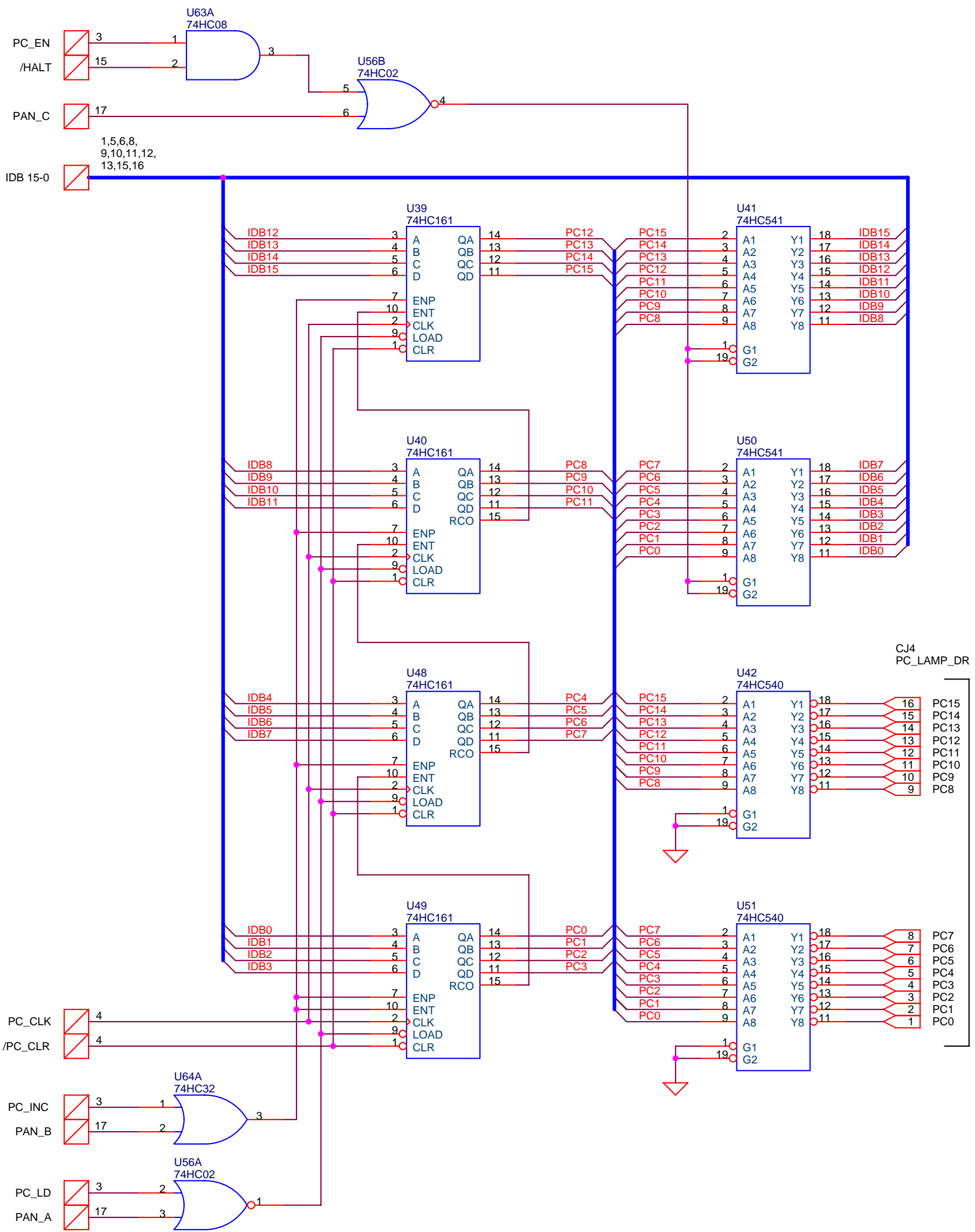
Title		
D16 PROCESSOR -- Run, Clock, and Clear Logic		
Size	Document Number	Rev
	DE000001	3.0
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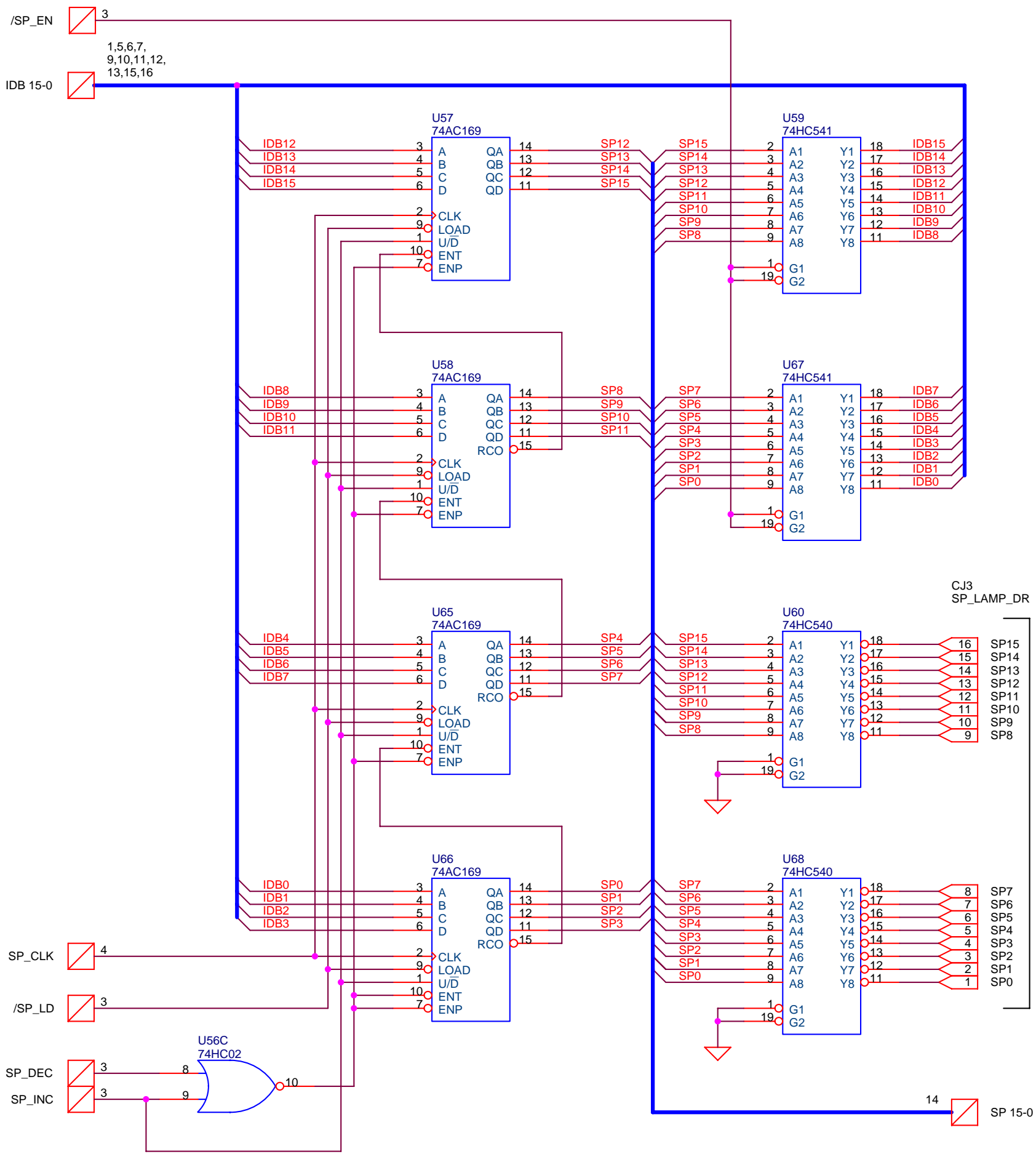
Title		
D16 PROCESSOR -- Switch Register I/F, Skip, Interrupt Logic, Internal Data Bus Termination		
Size	Document Number	Rev
	DE000001	3.0
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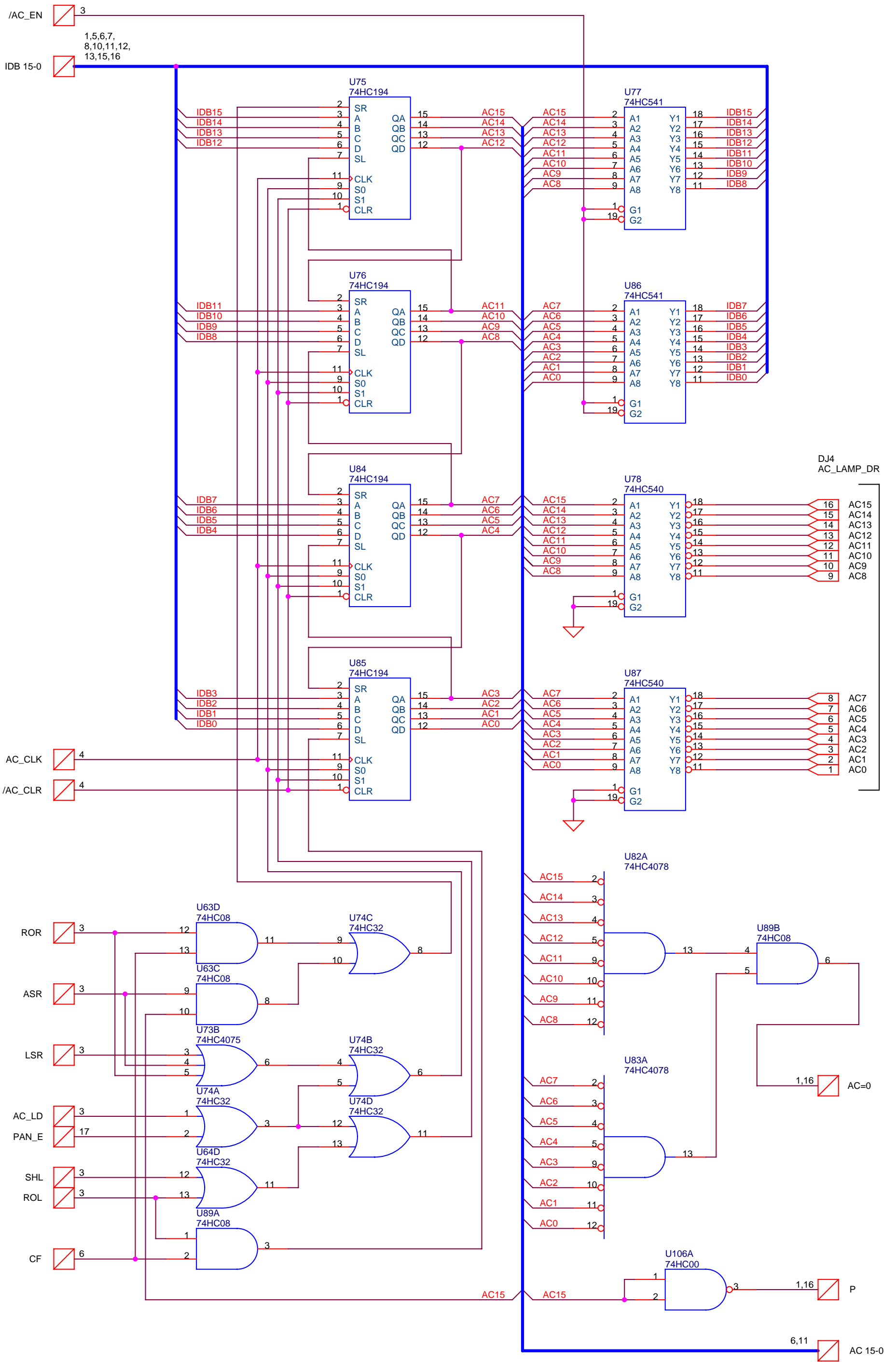
Title		
D16 PROCESSOR -- Flag Register		
Size	Document Number	Rev
	DE000001	3.0
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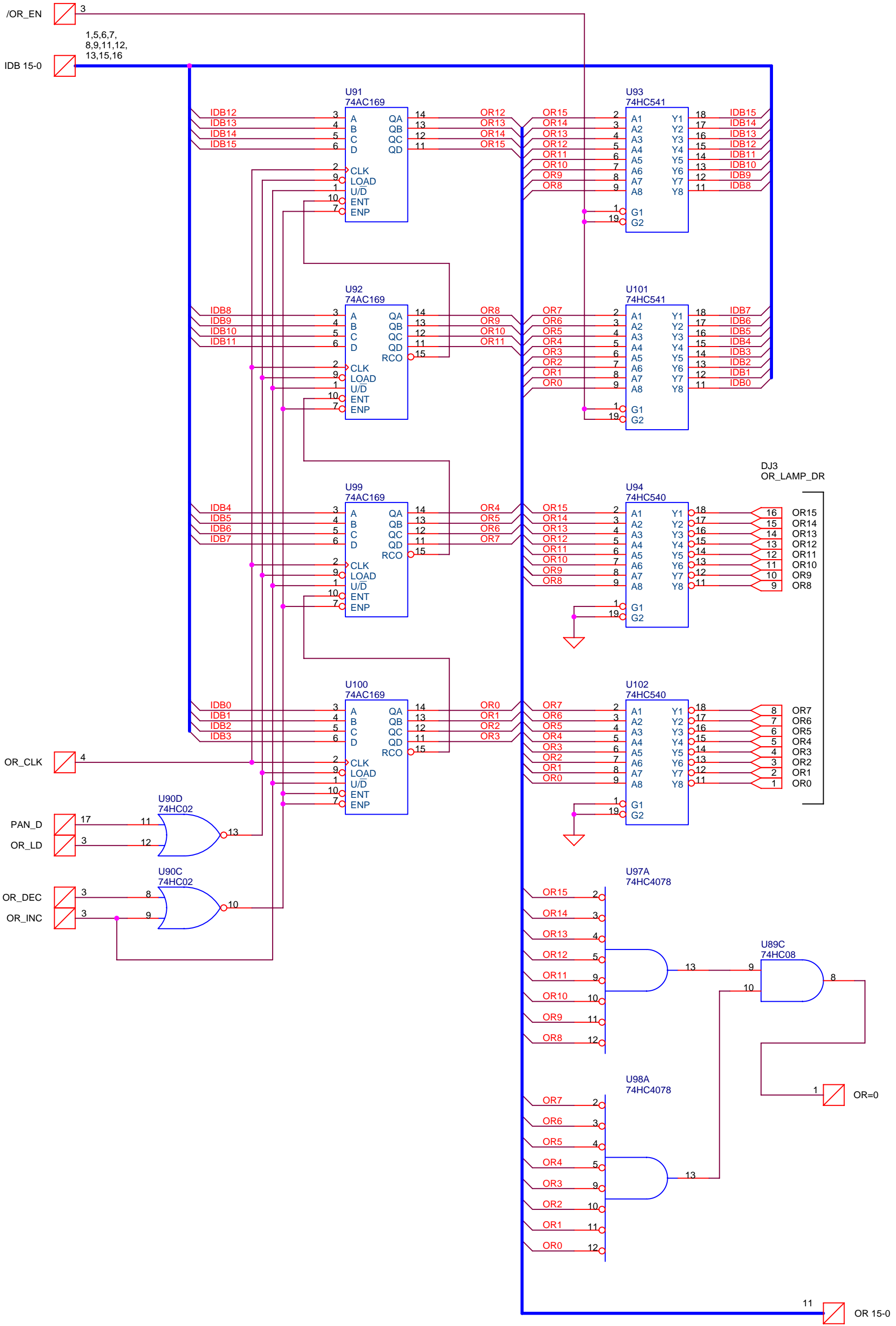
Title		
D16 PROCESSOR -- Program Counter Register		
Size	Document Number	Rev
	DE000001	3.0
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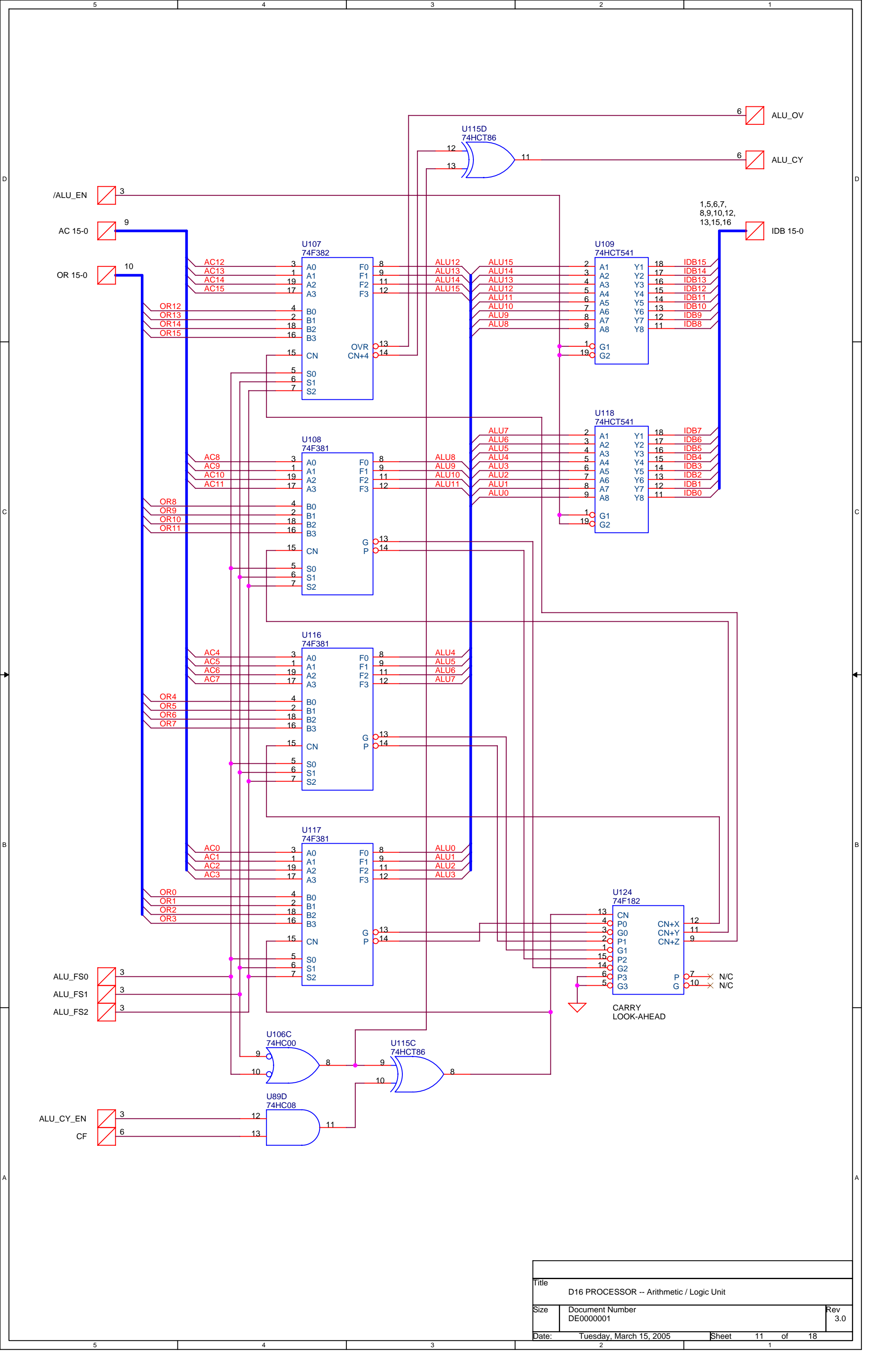
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D16 PROCESSOR -- Stack Pointer Register		
Size	Document Number	Rev
	DE000001	3.0
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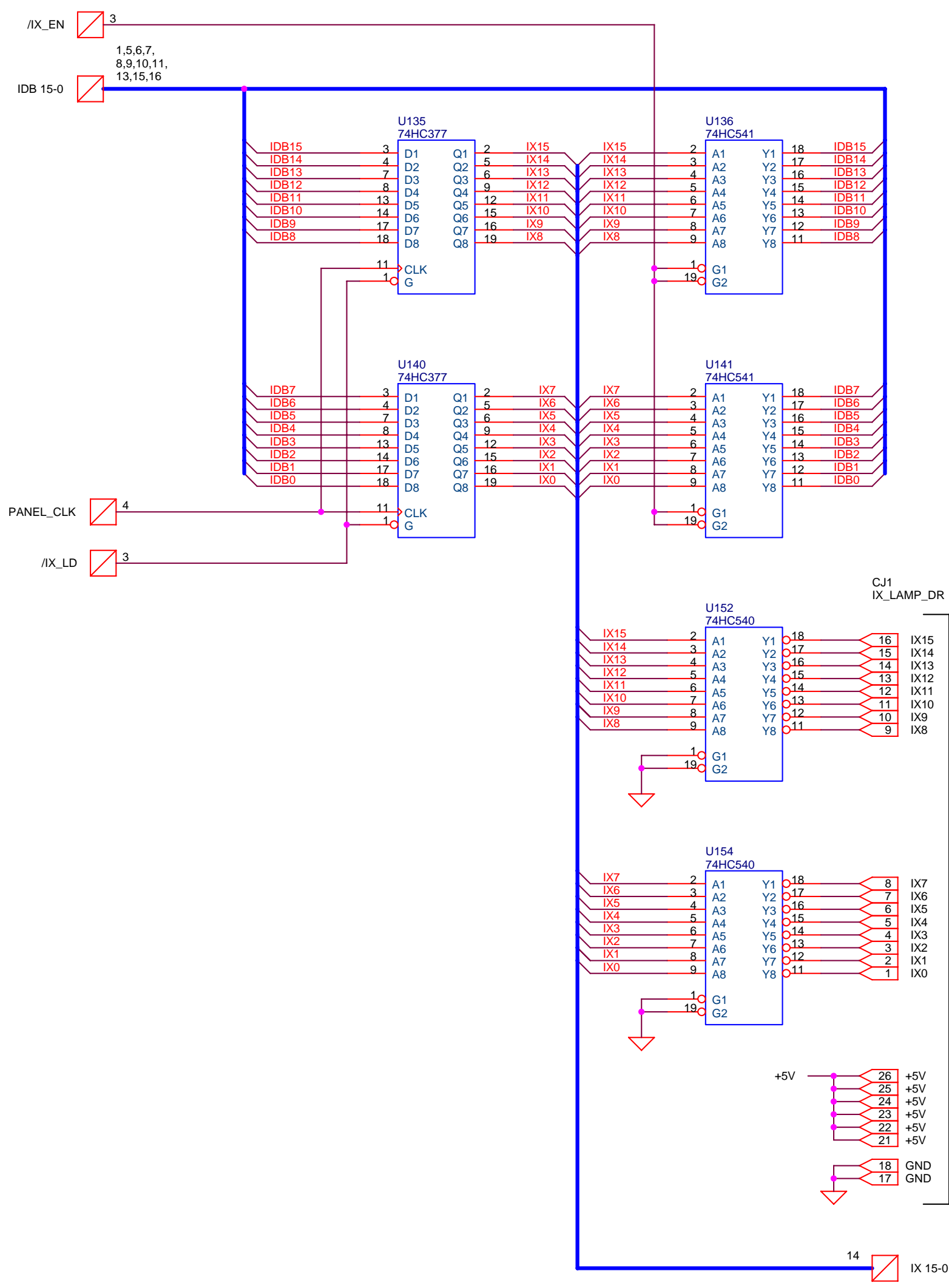
Title		
D16 PROCESSOR -- Accumulator Register		
Size	Document Number	Rev
	DE000001	3.0
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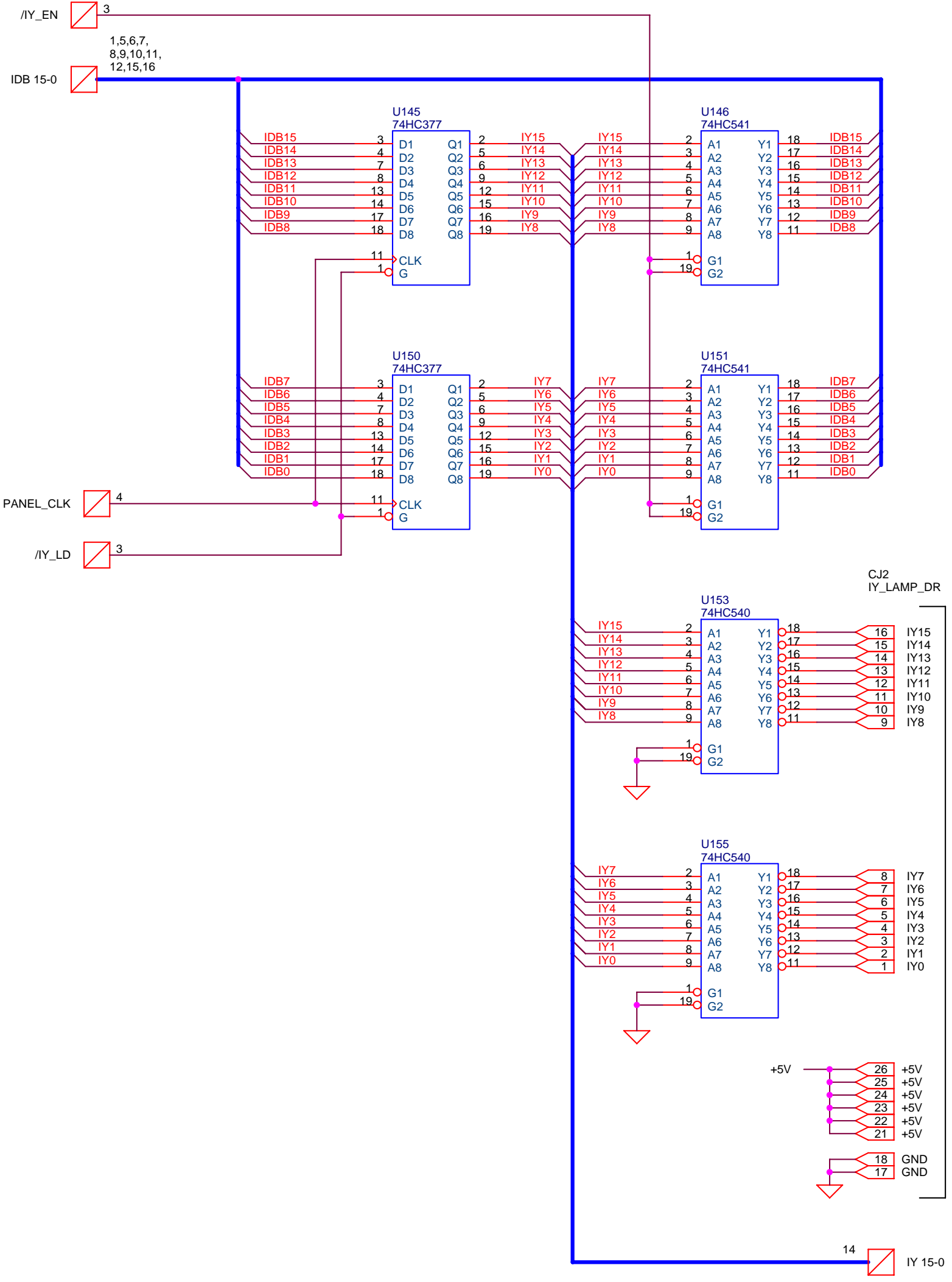
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D16 PROCESSOR -- Operand Register		
Size	Document Number	Rev
	DE000001	3.0
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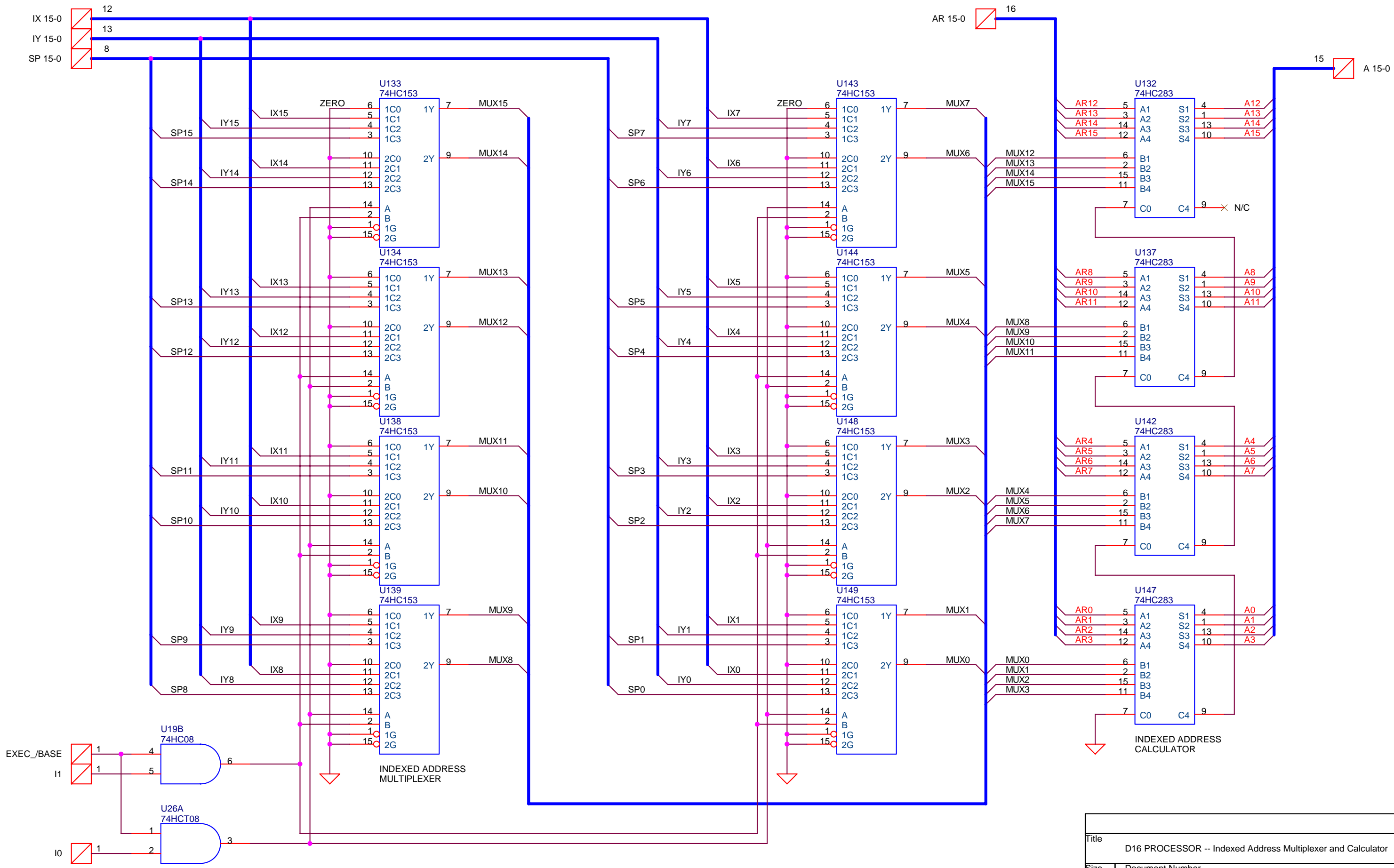
Title		
D16 PROCESSOR -- Arithmetic / Logic Unit		
Size	Document Number	Rev
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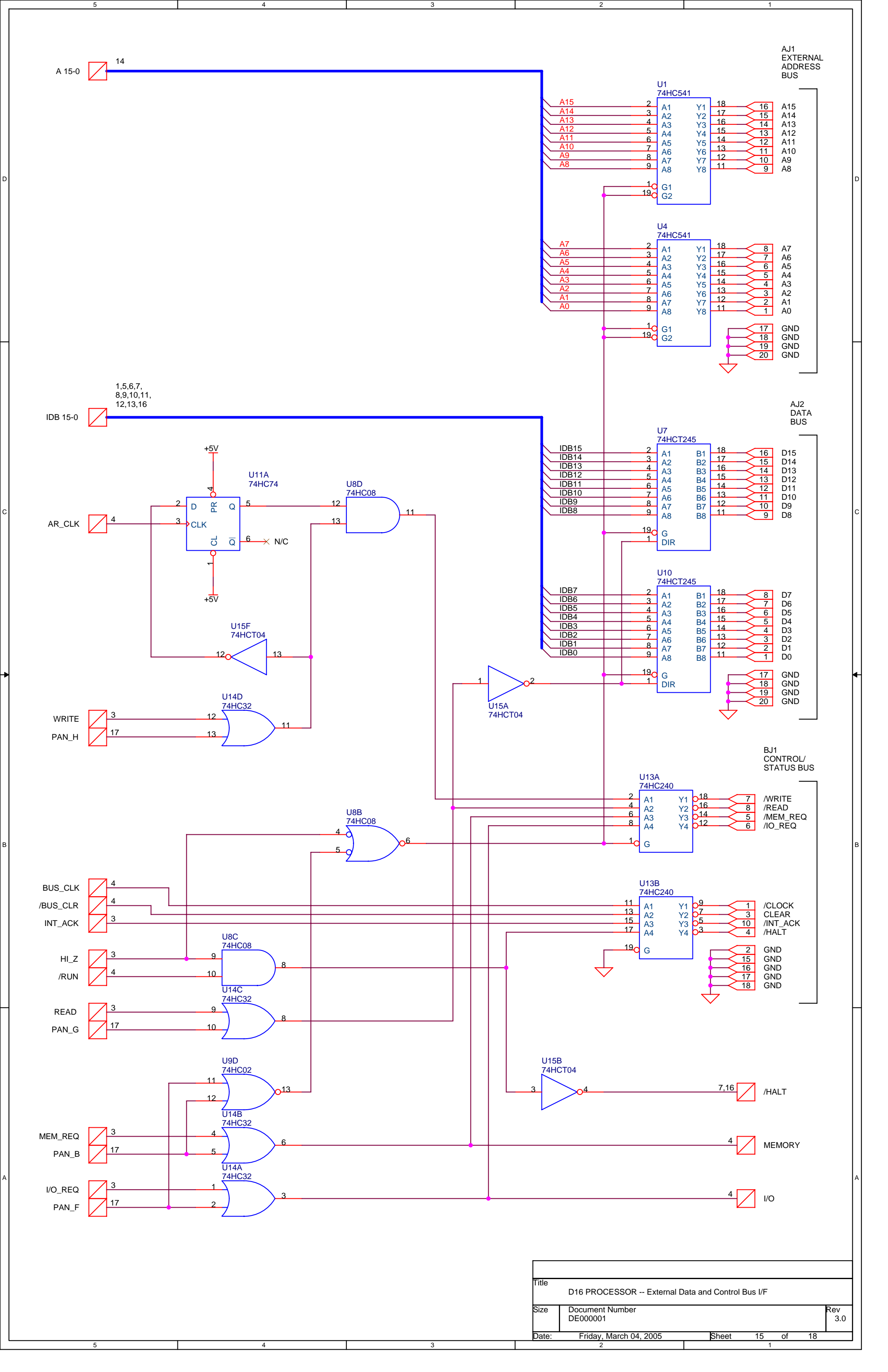
Title		
D16 PROCESSOR -- X Index Register		
Size	Document Number	Rev
	DE000001	3.0
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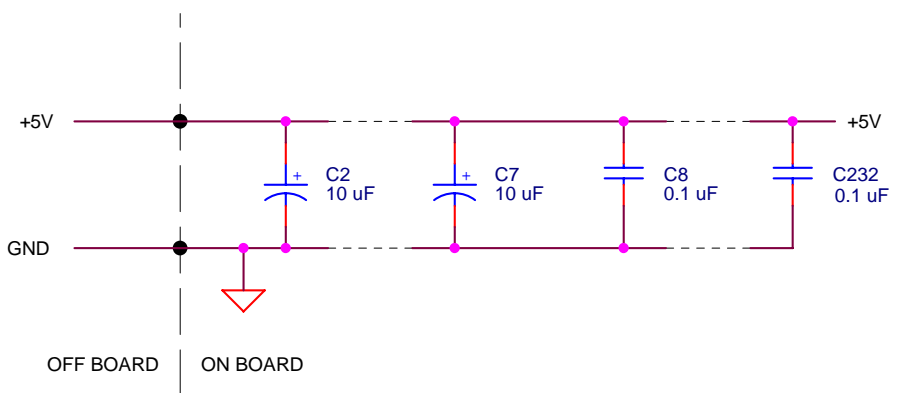
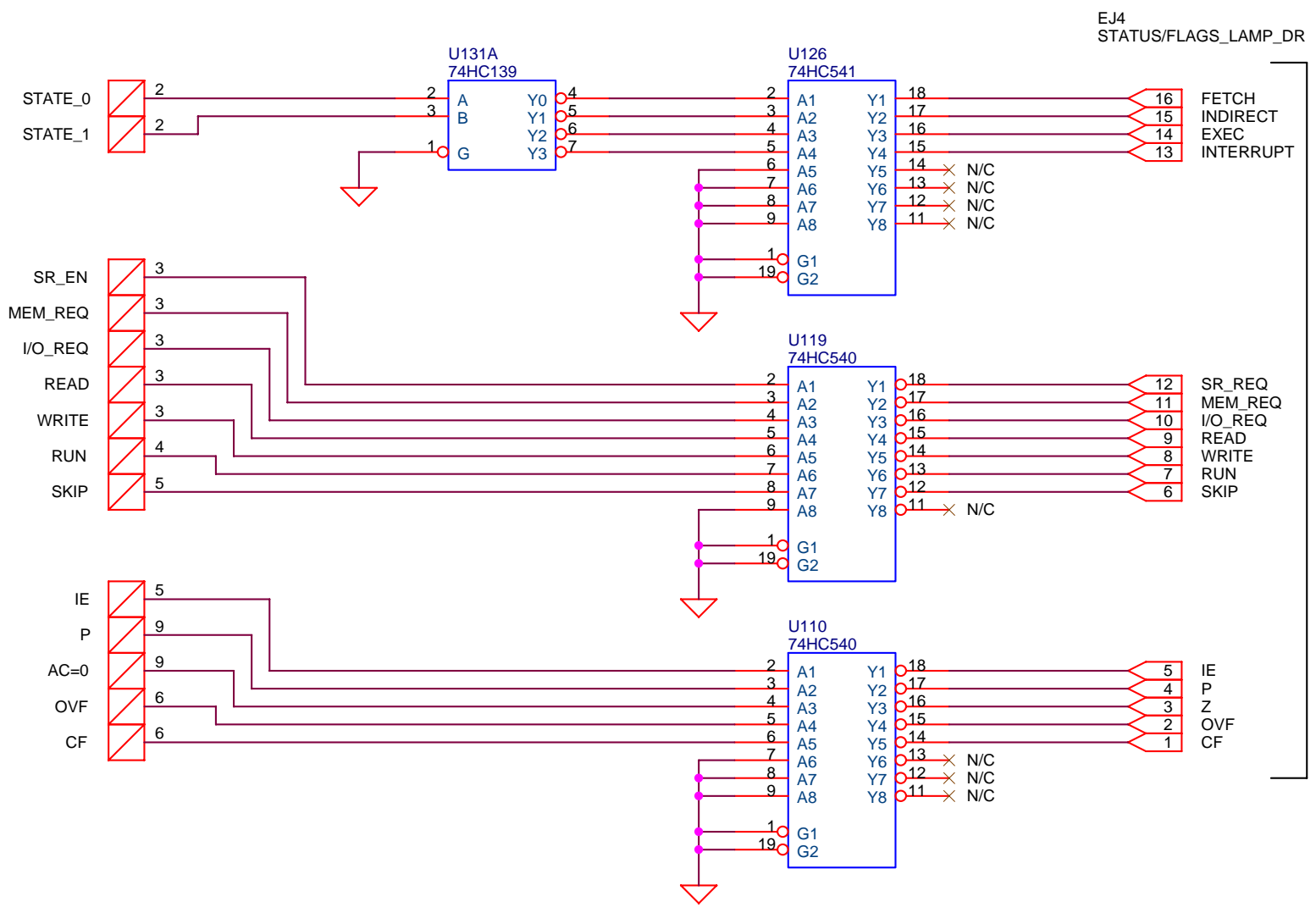
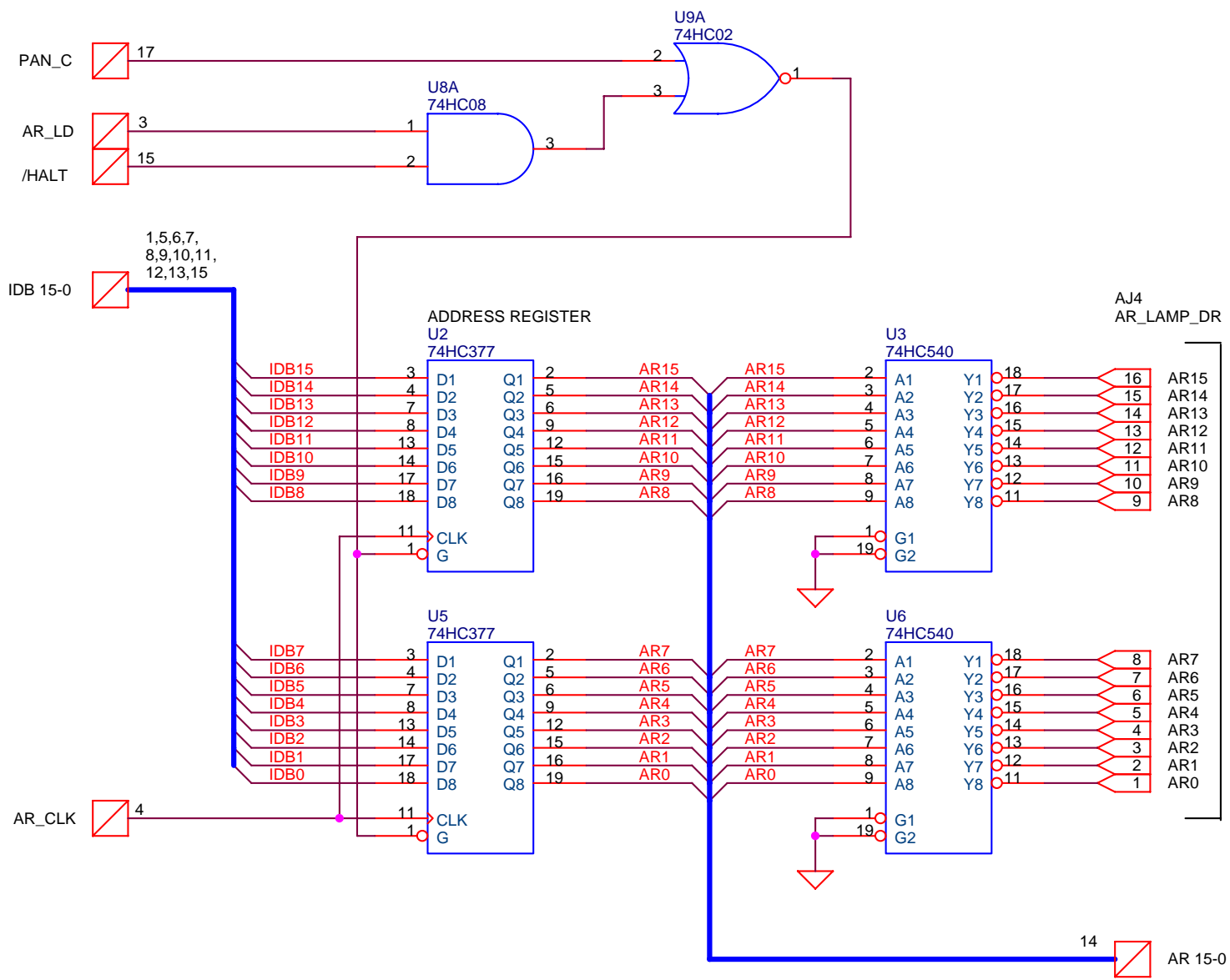
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D16 PROCESSOR -- Y Index Register		
Size	Document Number	Rev
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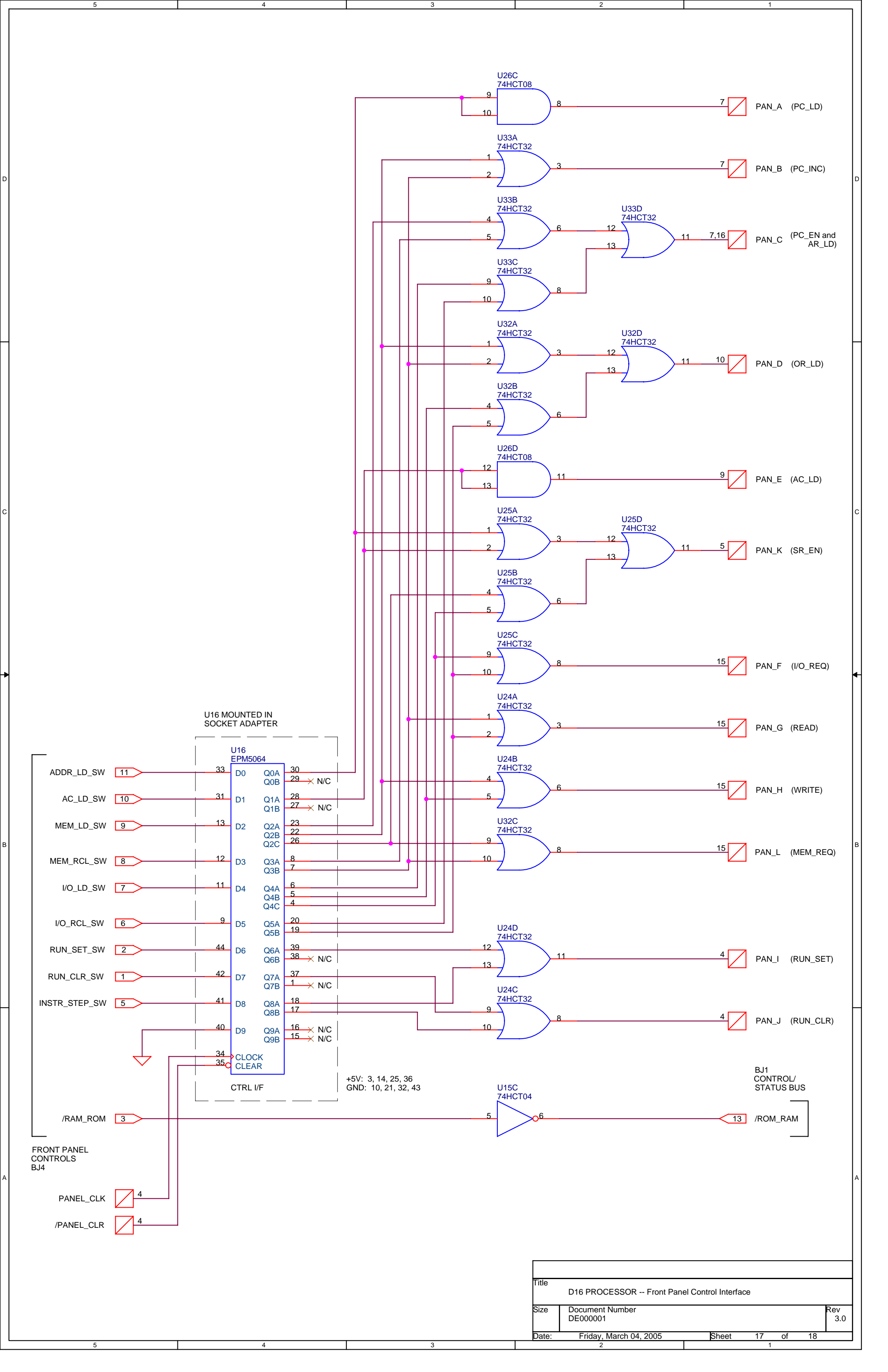
Title		
D16 PROCESSOR -- Indexed Address Multiplexer and Calculator		
Size	Document Number	Rev
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Title		
D16 PROCESSOR -- External Data and Control Bus I/F		
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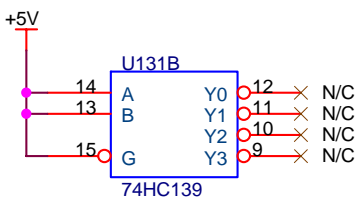
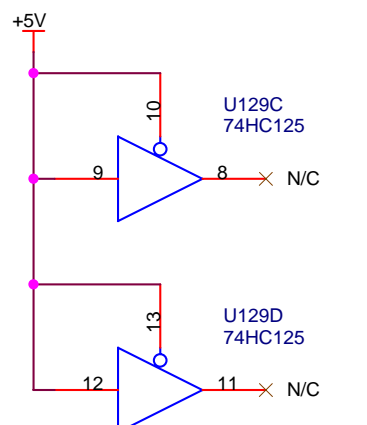
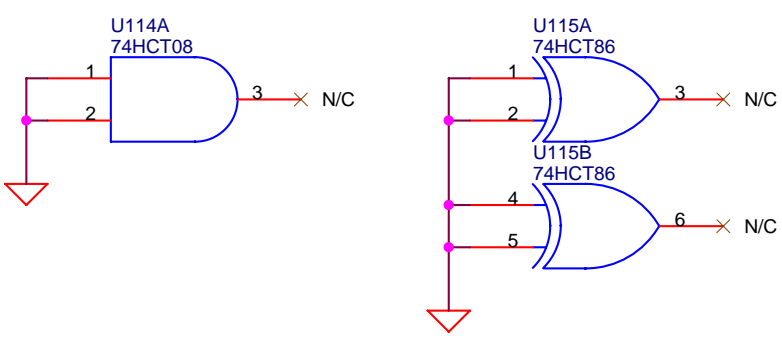
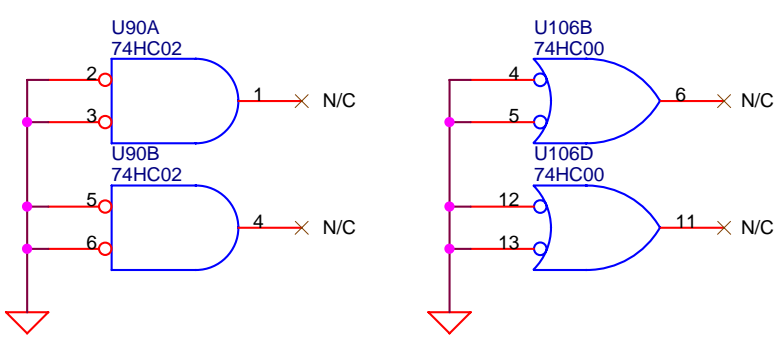
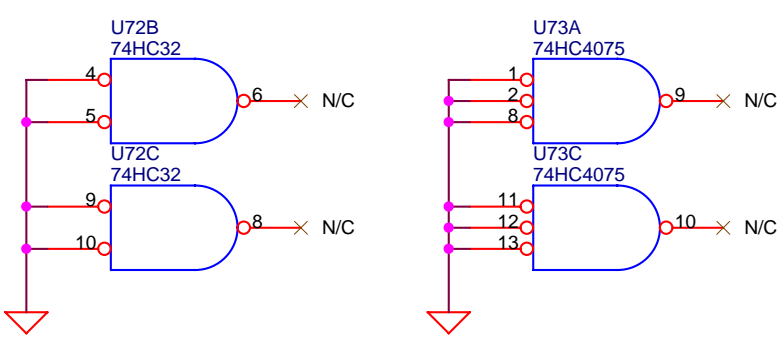
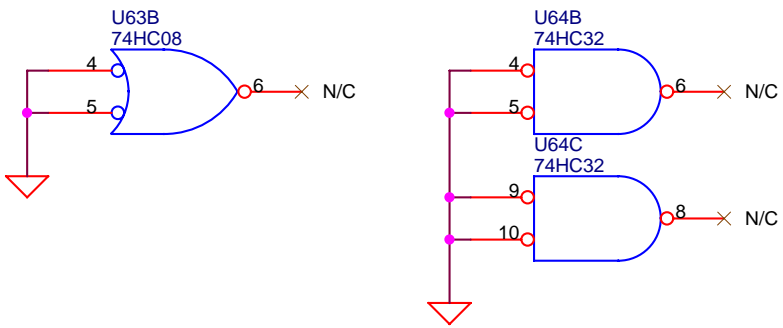
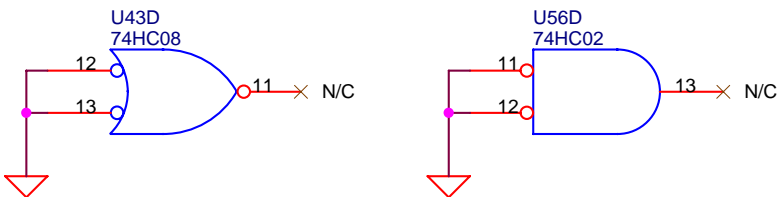
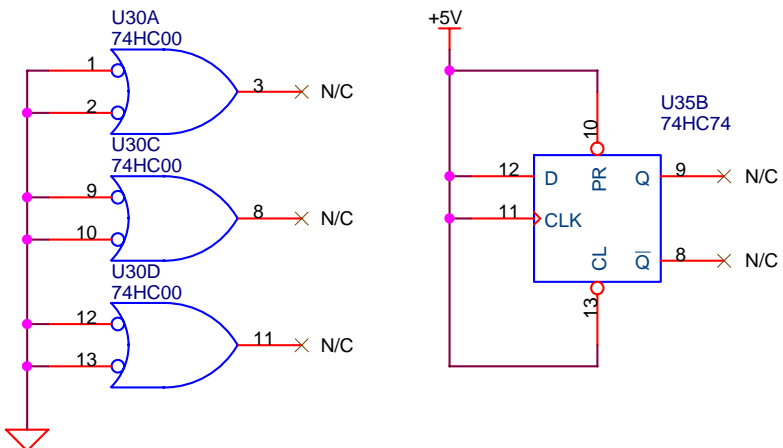
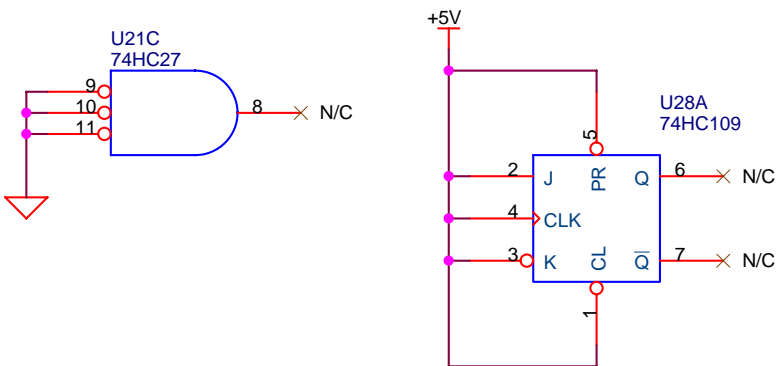
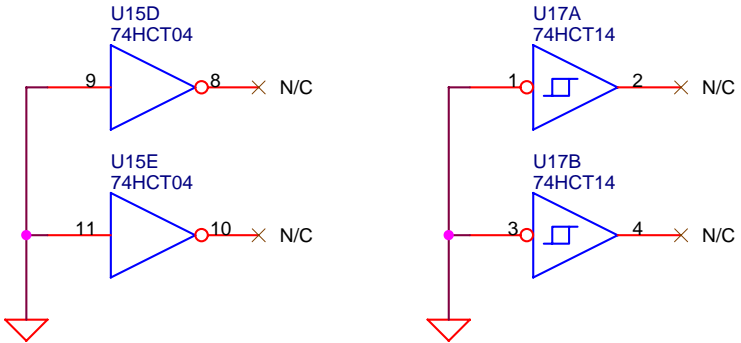
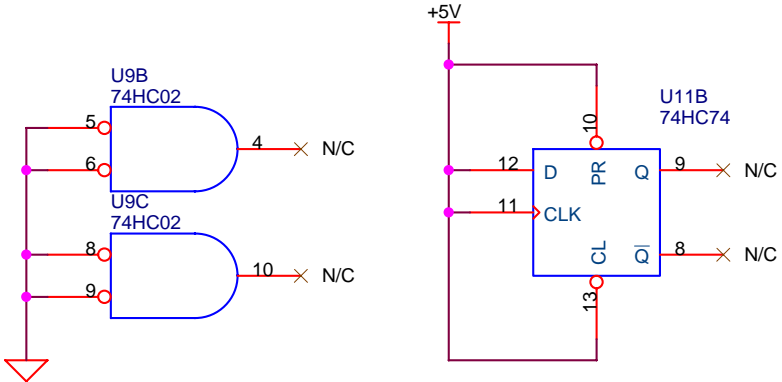


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Title		
D16 PROCESSOR -- Front Panel Control Interface		
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REV	DATE	CHANGES
1.0	02/14/03	BASIC COMPLETED DESIGN
2.0	03/17/03	CORRECTED DESIGN/DRAWING ERRORS AFTER DEBUG (EXCEPT CLOCK-EXTEND CIRCUIT) AS DETAILED IN DEBUG LOG *FIRST PUBLISHED REVISION*
2.1	03/11/05	CORRECTED ERROR IN CLOCK-EXTEND CIRCUIT; U19-13 AND U22-13 DISCONNECTED FROM U22-6 AND CONNECTED TO U22-7 U115 NOW 74HCT86; WAS 74HC86 U81 NOW 74HC08; WAS 74HCT08
3.0	06/23/05	UPGRADED CPU TO SUPPORT INDEXED ADDRESSING: ADDED INDEX REGISTERS IX AND IY (SHEETS 12 AND 13) ADDED MULTIPLEXER AND ADDRESS CALCULATOR (SHEET 14) ADDED MICROCODE ROM 8 FOR INDEX REGISTER FUNCTIONS (SHEET 3) CTRL SIGNAL AND DATA PATH CHANGES AS REQUIRED



- NOTES:
- 1: ALL RESISTORS IN OHMS, 1/4W 5%
 - 2: ALL CAPACITORS IN MICROFARADS
ALL POLAR CAPACITORS TANTALUM, 25V 10%
ALL NON-POLAR CAPACITORS CERAMIC, 50V 10%
 - 3: MICROCODE ROMS U61, U62, U79, U80, U103, U104, U120, U121, AND U157 70ns Tacc

Title D16 PRODESSOR -- Spare/Unused Circuit Elements		
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